

Abstract Submittal Information

Abstracts should be approximately 1/2 page long and include a brief introduction along with the key points that the paper will make.

Abstracts are due April 24, 2006. Late abstracts will be accepted for the Poster Session only. Acceptance letters will be sent May 15, 2006.

Industrial and Government Exhibits

Industrial exhibit reservations should be sent to mapld2006@klabs.org and should include company name and contact information (phone and e-mail). Please see <http://klabs.org/mapld06> for additional information.

Contact Information

For additional conference information, please visit klabs.org/mapld06 or contact:

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The 9th MAPLD International Conference is hosted by the NASA Office of Logic Design; sponsored by NASA Goddard Space Flight Center, National Security Agency, and NASA Electronic Parts and Packaging Program.

Important: Technology Transfer Considerations, Copyright, and Proprietary Information

Prospective authors are reminded that technology transfer guidelines and policies can considerably extend the time required for review of abstracts, presentations, and completed papers. Proper Government reviews, ownership of copyright, and the absence of proprietary information is solely the responsibility of the author. The organizing committee and their employing organizations will assume that all abstracts, presentations, and papers are appropriately cleared for unrestricted distribution to an international audience. All work submitted to MAPLD will be free of any copyright restrictions and marks and will be placed into the public domain for unlimited distribution and use.

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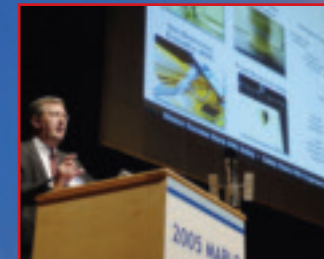


9th Military and Aerospace Programmable Logic Devices (MAPLD) International Conference

September 26-28, 2006

Ronald Reagan Building and
International Trade Center
with a session at the Smithsonian
National Air and Space Museum

Washington, DC



Call for Papers

Abstract Deadline
April 24, 2006

Conference Overview:

The 9th Annual International MAPLD Conference will address new developments on programmable logic devices and technologies, digital engineering, computing and related fields for military and aerospace applications. Papers are invited on a wide range of topics such as technologies, devices, processors, systems, high performance computers, logic design, reconfigurable computers, programming tools, standards, applications (e.g, flight, encryption, communications), fault tolerance, reliability, and radiation susceptibility.

Invited Speakers Include

Welcome and Opening Remarks
Chris Scolese, NASA Chief Engineer

Invited History Talk
Fred Haise, Apollo 13, Shuttle Approach and Landing Test (ALT)

Planned Program Sessions and Key Topics

- Applications: Military & Aerospace
- Logic Design and Processors
- Reconfigurable and Evolvable Computing
- Security
- Reliability and Failure Analysis
- Radiation and Mitigation Techniques
- Non-volatile Memory Technologies
- NESC and Software
- Digital Engineering and Computer Design - A Retrospective and Lessons Learned
- Reviewing and Verifying Designs
- Low Power Design

Planned Seminars include

- Spacewire 101
- Radiation and Programmable Logic Devices
- Systems Safety and Embedded Computing Systems
- Design Verification Tutorial

