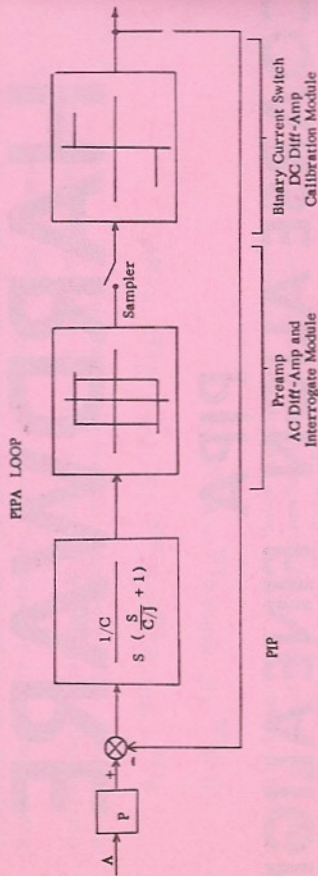


HARDWARE

PIPA
COARSE ALIGN - FINE ALIGN
ECDU
IRIG
OPTICS
OUA
AOT

PIPA CHARACTERISTICS

PARAMETER	CM	LM
1. Maximum measurable acceleration	19.1 g's	3.26 g's
2. Scale factor	5.85 cm/sec/pulse	1.00 cm/sec/pulse
3. Pendulosity	0.25 $\frac{\text{dyne cm}}{\text{cm/sec}^2}$	0.25 $\frac{\text{dyne cm}}{\text{cm/sec}^2}$
4. Torque to balance	4680 dyne cm	800 dyne cm
5. PIP float inertia	14.0 $\frac{\text{dyne cm}}{\text{rad/sec}^2}$	14.0 $\frac{\text{dyne cm}}{\text{rad/sec}^2}$
6. PIP viscous damping	$12 \times 10^4 \frac{\text{dyne cm}}{\text{rad/sec}}$	$12 \times 10^4 \frac{\text{dyne cm}}{\text{rad/sec}}$
7. PIP break point	8550 rad/sec	8550 rad/sec
8. PIP time constant	.117 ms	.117 ms
9. Total torque constant	.42 dyne cm/ma ²	.42 dyne cm/ma ²
10. Nominal torque current	105 ma	44 ma



- P = Pendulosity
- C = Coefficient of Viscous Damping
- J = Float inertia

The PIP Signal Generator Dicosyn provides information on rotor position in the form of a 3200 hertz output. The stable limit cycle in rotor position is converted to an AC suppressed carrier modulated signal.

The PIP Preamp amplifies the SG output (14 V/V gain) and phase shifts the 3200 hertz carrier 45° lag.

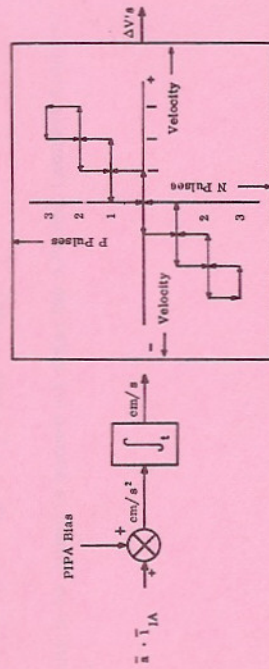
The AC Diff-Amp and Interrogate Module provides additional amplification (3050 V/V), peak detect float position, and sets a flip-flop. One state of the flip-flop indicates float position on the plus side of null and the other state means float position on the minus side of null.

The Binary Current Switch provides discrete current pulse outputs of appropriate phase as determined by the state of the flip-flop in the AC Diff-Amp Module.

The Calibration Module is the passive circuit interface between the ECS and the PIP torquer. The circuitry introduces bias and scale factor adjust capability into the PIPA loop.

The PIP Torque Dicosyn converts current pulses from the Calibration Module to torque about the PIP OA axis.

The DC Diff-Amp and PVA Module is the mechanism which regulates current and hence PIP torque to precisely controlled values.



- Each ΔV Command Module = 5.85 cm/s
- Each ΔV Lunar Module = 1.00 cm/s

SUSPENSION CHARACTERISTICS

1. Radial Force: 2.3 grams per .0001" min
2. Suspension current: 65 ± 6 ma
3. Phase angle of current: the two ends (SG & TG) matched with 3 ma lags 45° ± 2.8°
4. Suspension stiffness: 30 x 10⁻³ grams/micro-inch

TYPICAL TEMPERATURE CHARACTERISTICS

1. Scale Factor: 150 ppm/°F CM, 300 ppm/°F LM
 2. Bias: .05 cm/sec²/°F LM/CM
- °F Actual PIPA Temperature

PIPA PARAMETERS

Primary PIPA parameters are scale factor and bias. Specification values across ISS, G&N, and S/C testing are as shown in Table I-1.

Table I-1

Coefficient	PIPA Coefficient Stability Criteria			
	Units	D ₁	D ₂	D ₃
PIPA Bias (A _B)	cm/sec ²	0.50	0.70	0.90
PIPA Scale Factor SF	ppm	400	500	600

PIPA bias in a unity gravity field (A₀) must be within 0.30 cm/sec² of that evaluated in a zero gravity (a₀) field at the ISS level of test.

The maximum value of PIPA parameters which can be compensated for by the computer is as shown in Table I-2.

Table I-2

Coefficient	Units	Max Value (CM)	Max Value (IM)
A ₀	cm/sec ²	±9.14	±12.50
SF	ppm	±1900	±1900

PIPA COMPENSATION

- Register
- 1452 X PIPA Bias
- 1454 Y PIPA Bias
- 1456 Z PIPA Bias

PIPA Bias CM = (.0005569) (Reg Contents in Decimal) cm/sec²
 PIPA Bias IM = (.0007628) (Reg Contents in Decimal) cm/sec²

The correction to the PIPA's is

$$PIPA_C = (1 + SFE_I) PIPA_I - BIAS_I \Delta t$$

where

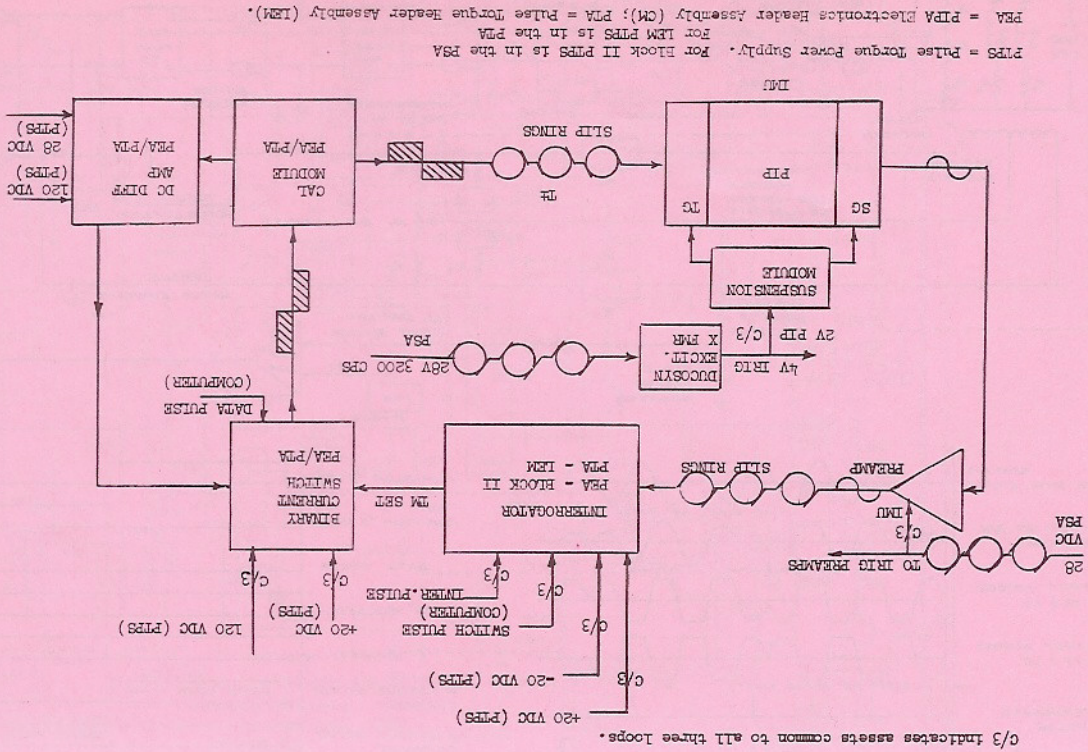
PIPA_C is the compensated data for the Ith PIPA denoted PIPAX_C, PIPAY_C, PIPAZ_C

$$SFE = \frac{SF - SF_{nom}}{SF_{nom}} \text{ (erasable load)}$$

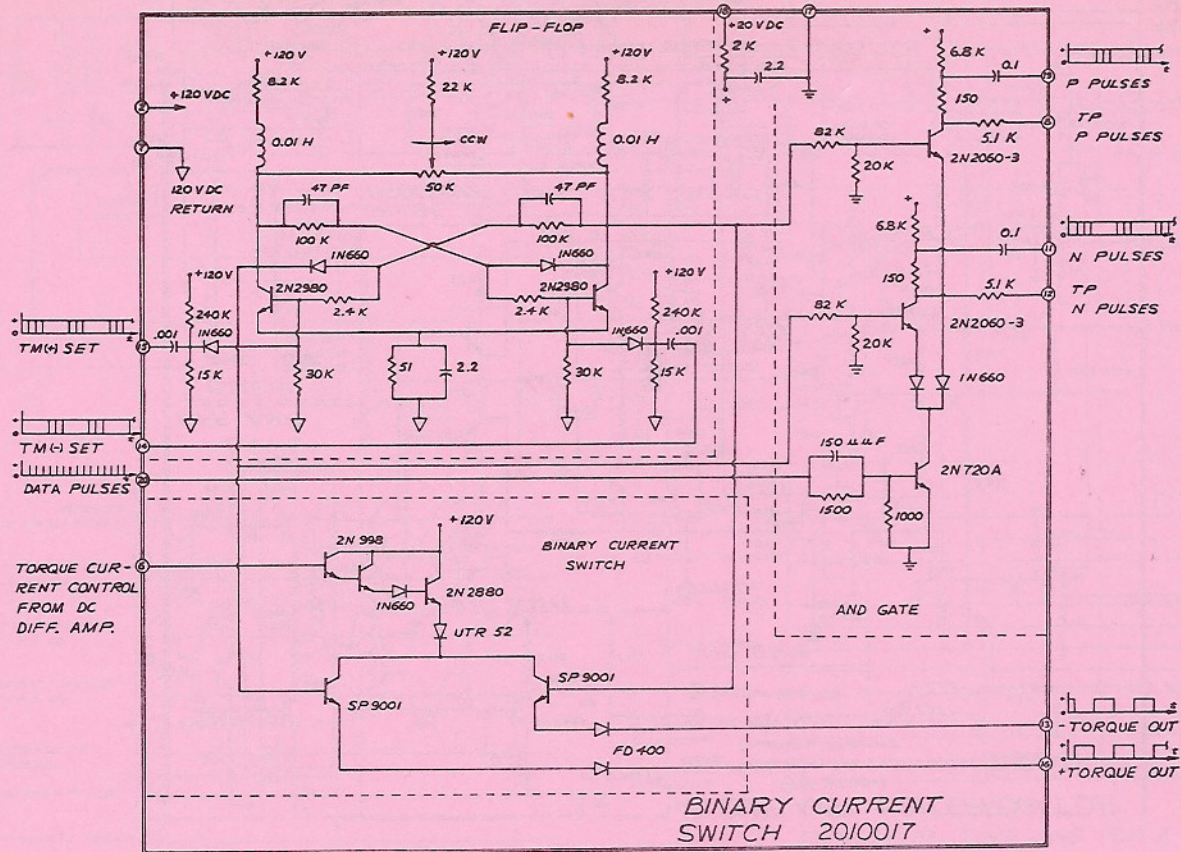
$$SF = \text{Scale-factor} \frac{\text{CM/Sec}}{\text{Pulse}}$$

BIAS_I is the bias for the Ith PIPA (an erasable load)

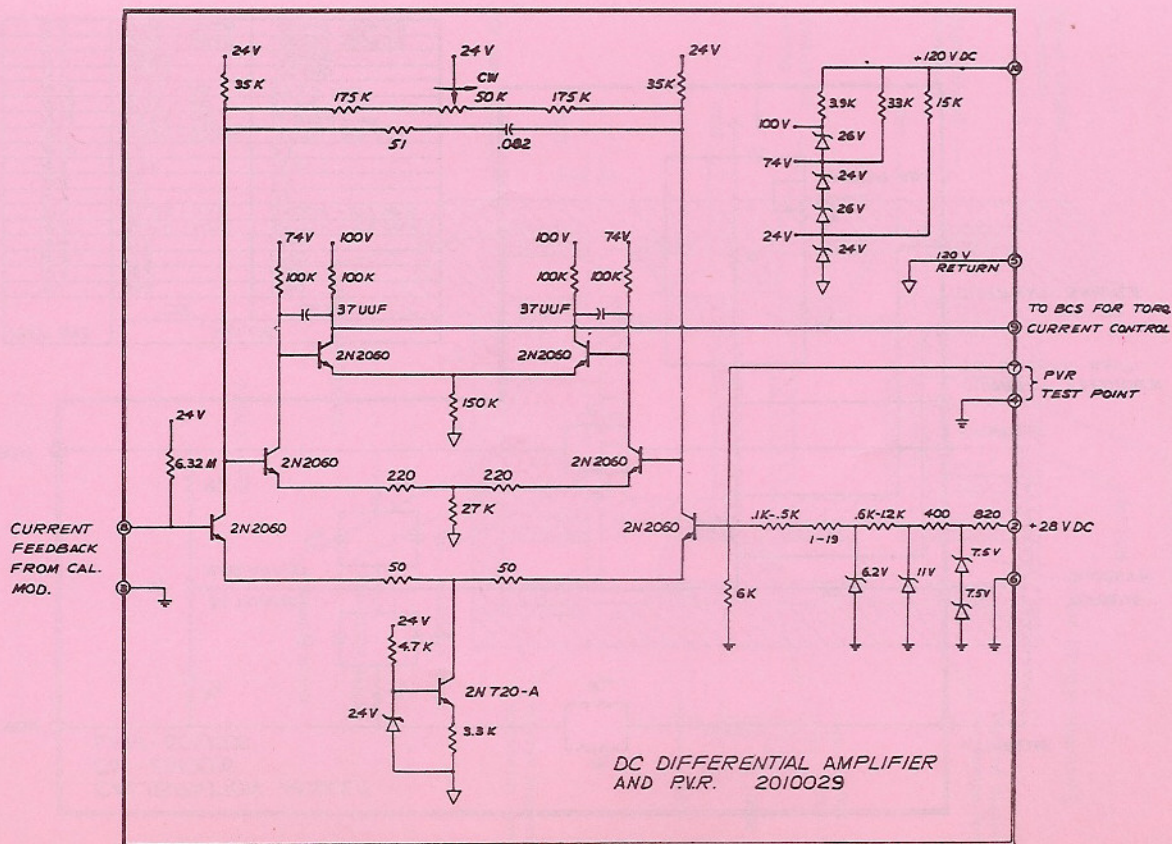
PIPA LOOP



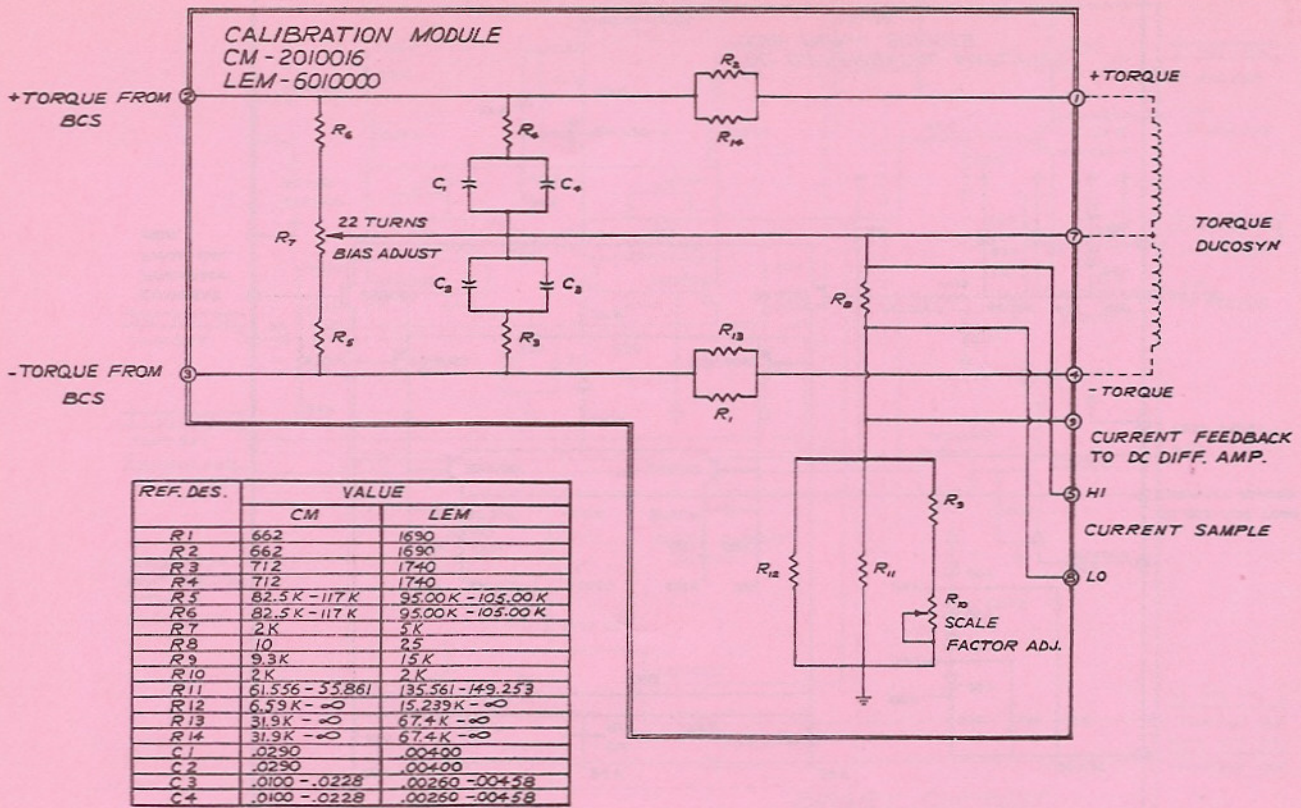
PIPS = Pulse Torque Power Supply. For Block II PIPS is in the PMA
 For LEM PTPS is in the PMA
 PEA = PIPA Electronics Header Assembly (CM); PMA = Pulse Torque Header Assembly (LEM).



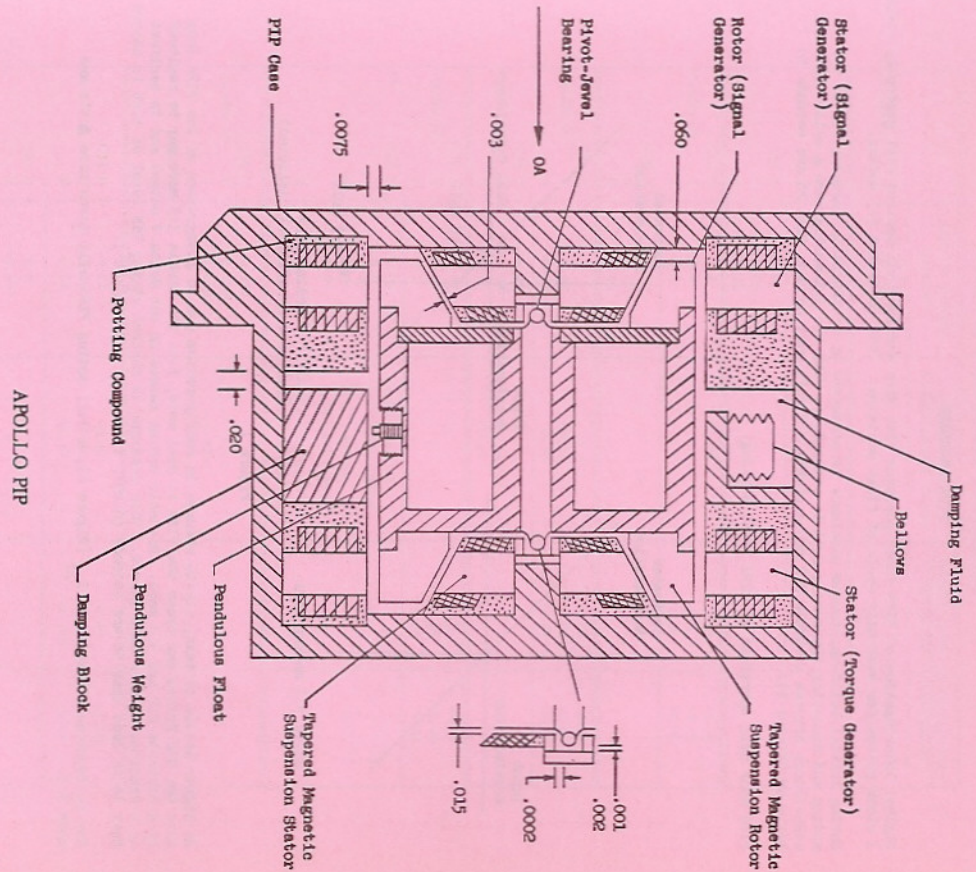
B-WH



B-WH



HW-10



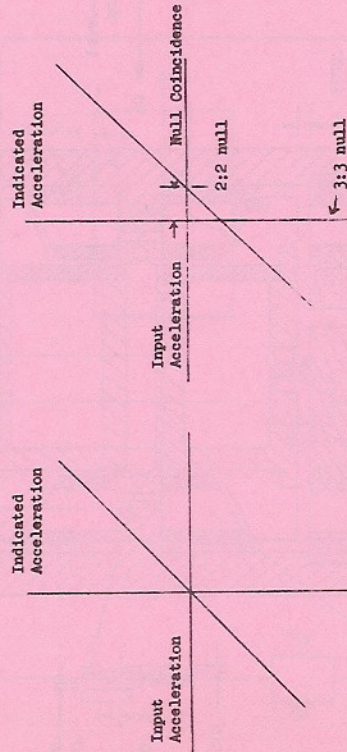
HW-11

PIPA DEADZONE

Digital interrogation of PIPA float position with zero acceleration applied will generally yield 3 clock pulses for each half cycle of float position. This is called 3:3 moding.

During initial build-up, torque unbalance is minimized by artificially inducing 2:2 or 4:4 moding and adjusting the zero g null to coincidence with the 3:3 moding zero g null. The null coincidence between a stable 2:2 and a stable 3:3 null must be less than 20 arc seconds in build-up (Figure 1).

PIPA near null operation at PIPA level testing.



Solid 3:3 moding PIPA

Solid 2:2 moding PIPA (2:2 mode artificially achieved)

Figure 1

At higher levels of test, subtle changes in configuration due to integration of the PIPA loop into the G&M System can cause the PIPA to dual mode, i.e. the stable 3:3 mode may be replaced by a bistable 2:2 and 3:3 mode. Bistable moding causes no net delta V output and is manifest as a deadzone to near null inputs. This deadzone is checked at the ISS level of test to assure that it is less than 50 arc seconds (0.0075 ft/sec² or 0.23 cm/sec²).

For an input acceleration of A₃₃ (Figure 2), a dual moding PIPA will issue zero ΔV's and mode 3:3.

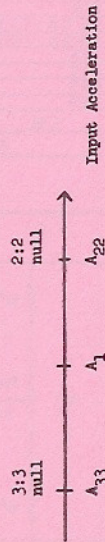


Figure 2

For an input acceleration of A₂₂, a dual moding PIPA will also issue zero ΔV's and mode 2:2. For an input acceleration between the A₃₃ and A₂₂ limits, the PIPA will issue zero ΔV's and dual mode from 2:2 to 3:3 at a rate of 5 or 6 times per second. The amount of time spent in 2:2 or 3:3 moding is dependent on the input acceleration A₁.

After Apollo 12, the biases of the LM PIPA's have been set at approximately -1.5 cm/sec². This prevents the PIPA's from entering the deadzone during the zero-g environment.

HISTORY OF IN-FLIGHT PIPA PERFORMANCE

	AVG IN-FLIGHT ERROR*	CHANGE FROM (PREFLIGHT) COMP	LUNAR ORBITAL DEVIATIONS	
			MINIMUM	MAXIMUM
Apollo 8	X +0.03 Y +0.00 Z	- - -	No Lunar Orbital Variations Available	
Apollo 9	CM X +0.02	+1.17	*** -0.39	-0.58
	Y -0.04	+0.28		
LM X	Z -0.08	-	+0.32	+0.42
	X +0.01	-	-	-
Y	Z +0.01	-	-	-
	X 0.0	-0.27	0.0	-0.36
Y	Z 0.0	-	0.0	-0.29
	X -0.06	-	0.0	+0.03
LM X	Z -0.12	-	-	-
	X 0.03	-	-	-
CM X	Z -0.02	-	-0.07	-0.31
	Y +0.03	-0.21	-0.25	+0.34
LM X	Z -0.01	-0.12	-0.01	+0.11
	X +0.02	-0.06	-	-
Y	Z -0.03	-0.17	-	-
	X +0.09	-	-	-
CM X	Z -0.05	-	0.0	+0.27
	Y 0.0	-	-0.08	-0.20
LM X	Z 0.0	-	-0.04	-0.25
	X -0.05	-0.65 Pre-DOI	-	-
Y	Z 0.0	-0.15 Post-Ascent	-	-
	X 0.0	0.0 Pre-DOI	-	-
CM X	Z 0.0	-0.18 Post-Ascent	-	-
	X 0.0	-0.06 Pre-DOI	-	-
Y	Z 0.0	+0.29 Post-Ascent	-	-
	X -0.204	-	-	-
LM X	Z -0.195	-	-	-
	X +0.006	-1.63	-	-
CM X	Z +1.50	-	-	-
	Y -1.35	-	-	-
LM X	Z -1.52	-	-	-
	X -0.02	-0.10	-0.34	-0.55
Y	Z -0.02	+0.08	-0.18	0.13
	X -0.02	+2.77	-0.185	-0.45
LM X	Z 0.3	-	0.04	0.13
	Y -0.35	-	-0.09	0.16
CM X	Z -0.14	-	-0.18	-0.34
	X 0.03	0.20-Pre-Ascent	-	-
Y	Z -0.04	0.36 Post-Ascent	-	-
	X -0.05	0.34 Pre-Ascent	-	-
LM X	Z 0.01 Post-Ascent	- Post-Ascent	-	-
	X 0.0 Pre-Descent	- Pre-Ascent	-	-
Y	Z -0.02 Pre-Descent	-0.16 Post-Ascent	-	-
	X 0.03 Pre-Descent	-0.03 Pre-Ascent	-	-
Z	X -0.01 Post-Ascent	-0.01 Post-Ascent	-	-
	Y 0.0	-	-	-

* Deviations from compensation updates when made, or from preflight loads when no update was made. All units cm/s²

** This IMU was turned off for ~27 hours on the lunar surface.

*** Earth orbit deviations.

NOTE: Variations of CM bias in lunar orbit caused by variations in coolant temperature.

IMU COARSE ALIGN LOOP

The coarse align loop drives the IMU gimbals to the angles commanded by the computer with an accuracy of $\pm 1.5^\circ$. The coarse align mode also sets as a caging mode when a gimbal lock condition is approached. The three basic elements of the coarse align loop are the Digital Computer which issues angle commands ($\Delta\theta_c$) and mode setting, the IMU, and the ECDU which encodes gimbal position and provides position and rate feedback for proper loop operation.

IMU FINE ALIGN LOOP

The fine align loop drives the IMU gimbals to the computer commanded angles $\pm 80''$ by pulse torquing the error. Pulse torquing is a computer controlled switching of a constant current source to a gyro torque winding. The current produces gyro torque, a corresponding precession rate, and hence, gimbal position.

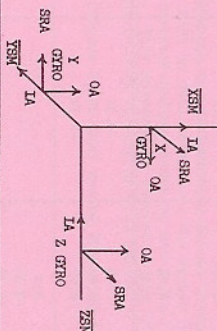
IMU INERTIAL MODE

The elements of the inertial mode are the gyro, stabilization amplifier, and gimbal torque motor. The gyro senses inertial rotation about its input axis and supplies torquing to the gimbal via the stabilization amplifier to compensate for the motion.

APOLLO INERTIAL INSTRUMENTATION/STABLE MEMBER ORIENTATION DIAGRAM

Drift Rate About SM Axes	HRD	Gyro Drift Coefficient	ADRRA
$\dot{\theta}_{XSM}$	$+\dot{\theta}_{HX}$	$+\text{ADJAX } (\theta_{XSM})$	$-\text{ADRSAX } (\theta_{XSM})$
$\dot{\theta}_{YSM}$	$+\dot{\theta}_{HY}$	$+\text{ADJAY } (\theta_{YSM})$	$-\text{ADRSAY } (\theta_{YSM})$
$\dot{\theta}_{ZSM}$	$-\dot{\theta}_{HZ}$	$+\text{ADJAZ } (\theta_{ZSM})$	$+\text{ADRSJAZ } (\theta_{ZSM})$

θ_{XSM} , θ_{YSM} , θ_{ZSM} is positive along positive stable member axes.

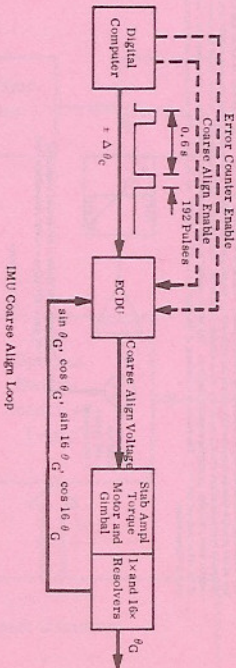


Q/TO	IA ALONG	SRRA ALONG	OA ALONG
X	$+\dot{\theta}_{XSM}$	$-\dot{\theta}_{XSM}$	$+\dot{\theta}_{ZSM}$
Y	$+\dot{\theta}_{YSM}$	$-\dot{\theta}_{YSM}$	$+\dot{\theta}_{ZSM}$
Z	$-\dot{\theta}_{ZSM}$	$-\dot{\theta}_{ZSM}$	$+\dot{\theta}_{ZSM}$

Input axes of the X, Y, Z accelerometers lie respectively along positive XSM, YSM, ZSM axes.

IMU COARSE ALIGN LOOP

The servomechanism used to achieve coarse align of the block II IMU is shown below.



Computer Operation

The following sequence of events is performed by the computer each time the Coarse Align program is entered.

1. Issue Coarse Align Enable. The computer sets Bit 4 of Channel 12 which issues the Coarse Align Enable discrete to the ECDU.
2. All three IMU axes of the ECDU.
3. Determine Command Angle. The contents of the computer gimbal angle counters are read and differenced with the desired gimbal angle to determine the command angle.
4. Issue Command Angle. Gimbal angle commands are issued in bursts of 192 pulses (8.4 degrees) at a rate of 3,200 pulses/second. Each burst requires 60 milliseconds. The delay between bursts is 540 milliseconds. If the command integral multiple of 192 pulses, the remainder is issued as the last command. Gimbal commands are gated out of the computer by gating Bits 13, 14, and 15 of Channel 14. All three bits are set simultaneously, thereby allowing all three gimbals to be slewed simultaneously.
5. Determine If Command Angle is Achieved. After gimbal commands have been issued, the computer reads gimbal position and compares the position achieved with the desired angle. If the difference is greater than 2.0 degrees, an alarm is issued.

Steps 1 through 5 constitute a computer control process which positions gimbals in an open loop sense. The computer does not check final gimbal position much that corrective commands can be issued if the desired position has not been reached. The check on final gimbal position is used to determine if an alarm condition exists.

ECDU Response to Coarse Align Enable

When addressed by the Coarse Align Enable discrete from the computer, the ECDU:

1. Provides a ground to the PSA coarse align input relays, thereby switching the stabilization amplifier inputs from HRD outputs to the ECDU DAC output.
2. Supplies a ground for the PSA stabilization amplifier demodulator relay, thereby changing the reference excitation of the stabilization amplifier ring demodulators from 5,250 Hz to 500 Hz.
3. Provides grounds for the coarse align relay in each of the stabilization amplifiers, thereby changing stabilization amplifier servo compensation.
4. Generates an internal timing signal which enables gimbal feedback pulses ($\Delta\theta^2$) to increment the error counter. This signal also changes the read counter high speed pulse rate (P1 pulses) from 12.8 kpps to 0.4 kpps.

ECDU Response to Error Counter Enable

When addressed by the Error Counter Enable discrete from the computer, the ECDU:

1. Generates an internal timing signal which allows error counter pulses ($P_E = \Delta\theta^2$ or $\Delta\theta_c$) to increment the error counter. Absence of this discrete zeros the error counter and holds it there.
2. The Error Counter Enable command (E) is combined with the Coarse Align discrete (C_A) and the CDU Zero command (CDUZ) to mode the read counter in accordance with the following logic equation.

$$Y = \bar{E} \cdot C_A + \text{CDUZ}$$

where

- a. The presence of Y inhibits P1 pulses from incrementing the read counter
- b. \bar{E} = Error Counter Enable "not"
- c. C_A = Coarse Align Enable
- d. CDUZ = CDU Zero Discrete
- e. $E \cdot C_A$ is an "and" operation
- f. \bar{E} is an "or" operation

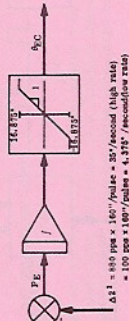
Inhibiting the read counter in this fashion will prevent gimbal drift due to residual voltages within the loop. The fine error will accumulate to a value such that the effect of residual voltages is cancelled.

ERROR SIGNAL

Serving the IMU in the correct position as commanded by the computer is achieved by a multiple loop system consisting of the error signal, a DAC, the coarse align mixing amplifier, a system of rate and switch selection logic, the read counter, and the stabilization loop.

READ COUNTER

The error counter provides digital differencing between gimbal commands and gimbal positions. Input pulses to the error counter are either position command pulses ($\Delta\theta_c$) from the computer or gimbal position feedback pulses ($\Delta\theta_f$) from the read counter loop. Error counter pulses ($\Delta\theta_e$) cause the error counter to count up. Read counter ($\Delta\theta_r$) pulses will, in turn, count the error counter down. When the error counter reaches zero, the error counter will output a "1". The result is a limiting of the output at 11.25° = 6.489° or 16.875°. The 1681 functional characteristic is shown below.



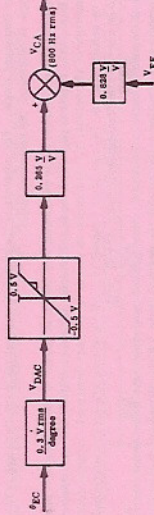
Error Counter

DAC (DIGITAL TO ANALOG CONVERTER)

The DAC consists of logic switches driven by the error counter, a divide down resistive ladder network, and a scaling amplifier. The function of the DAC is to provide a 600 Hz signal whose amplitude is proportional to the angle content of the error counter. DAC sensitivity is adjusted to 0.3°/Volt/degree.

COARSE ALIGN MIXING AMPLIFIER

The coarse align mixing amplifier adds the fine error voltage and the DAC output with a mixing ratio of 2 to 1 in favor of the fine error input. DAC outputs to the coarse align mixing amplifier are color-limited to 0.1 volt as shown below.



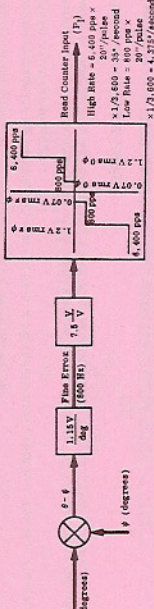
Coarse Align Mixing Amplifier

FINE SYSTEM SWITCH SELECTION LOGIC

The fine system switch selection logic is a network of weighting resistors, summing amplifiers, and switches used to implement the trigonometric identity $\sin(\theta - \psi) = \sin\theta \cos\psi - \cos\theta \sin\psi$. Functionally, this network compares the gimbal angle (θ) as represented by the 18-appeared receiver signals to the read counter angle (ψ) and generates an error signal used to drive the read counter loop.

RATE SELECT AND UP/DOWN LOGIC

This select and up/down logic is generated from the fine error signal. The scaling magnitude of the error between read counter angle and read counter angle (ψ) is selected by the type from decoder. The error counter ($\Delta\theta_e$) is selected by the type from decoder. The output of the Schmitt trigger to determine at which rate the read counter should be incremented. If the output of the high speed Schmitt is high (1.7-volt trigger threshold), the read counter is incremented at 6,400 pps. If the output of the fine Schmitt is high (0.1-volt trigger threshold), the rate is 800 pps. If the output of the low speed Schmitt is high, the read counter will be incremented 1 bit at low speed or 8 bits at high speed. The functional equivalent of system data and switch selection is shown below.



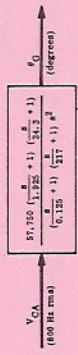
Read Counter

The read counter is a 18-stage digital counter with an IMU angle granularity of 20 seconds/deg. Inputs to the read counter are commanded by the computer to the low order bit (LSB). Assuming the error counter is in the correct position to the correct gimbal position, the read counter is represented as an integrator with a count rate in degrees/second and an output in degrees.

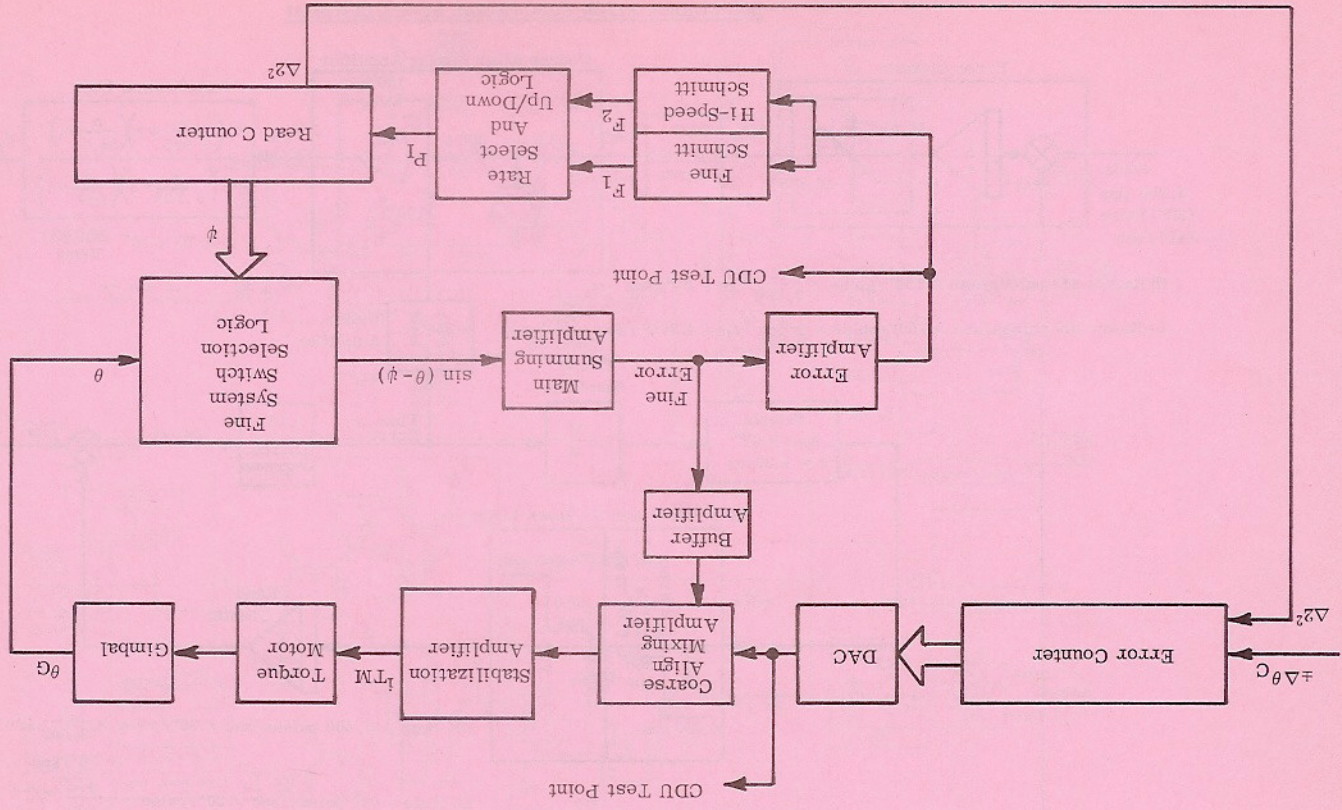


STABILIZATION LOOP

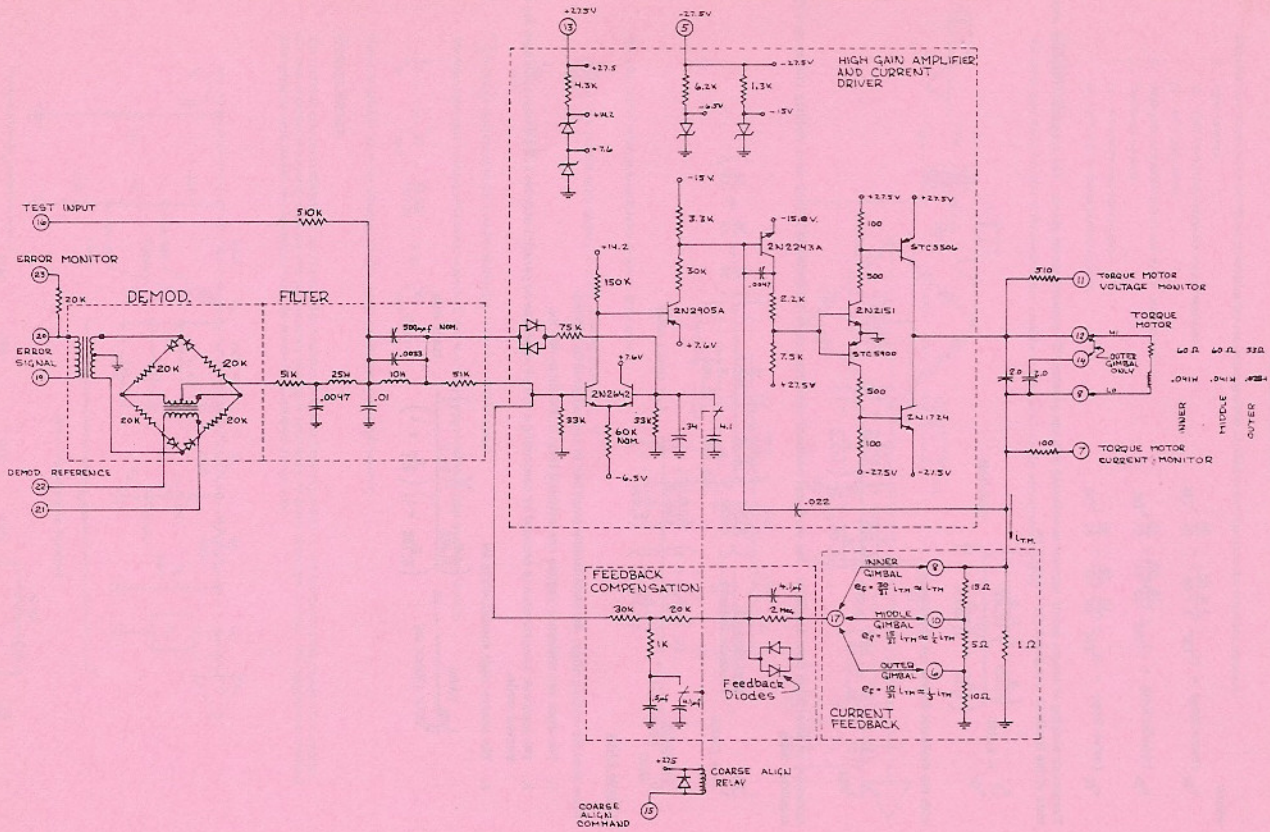
The scaling part of the coarse align compensator consists of the stabilization amplifier, torque motor, and gimbal. The overall transfer function of the above three elements is as shown below.



IMU COARSE ALIGN BLOCK DIAGRAM

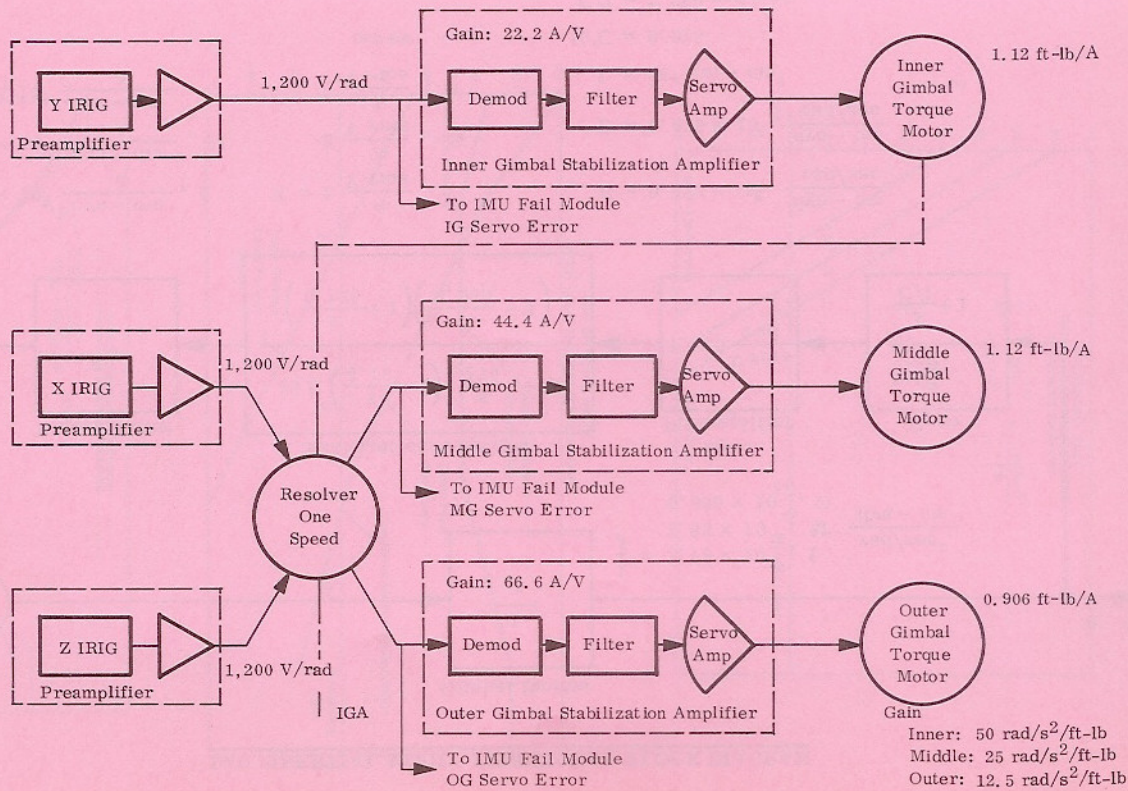


IMU STABILIZATION AMPLIFIER



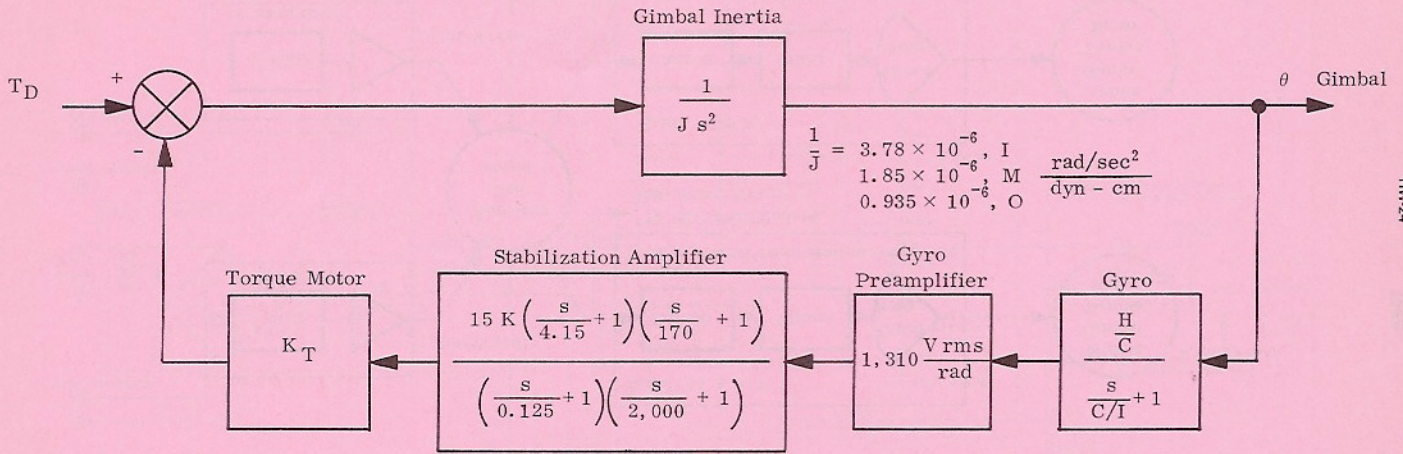
HW/22

IMU "INERTIAL MODE" BLOCK DIAGRAM



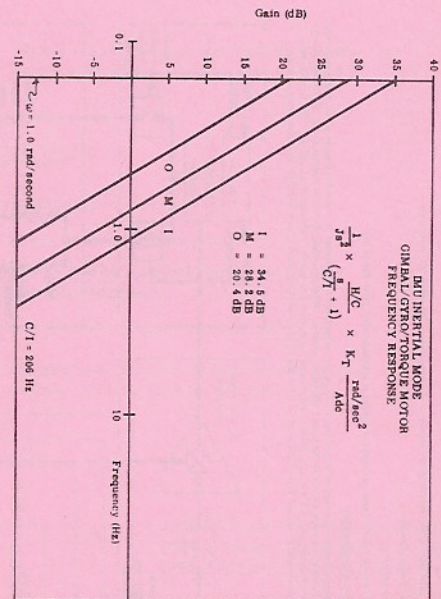
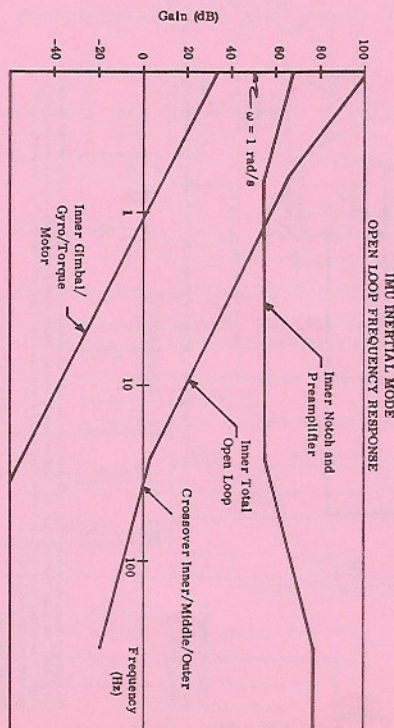
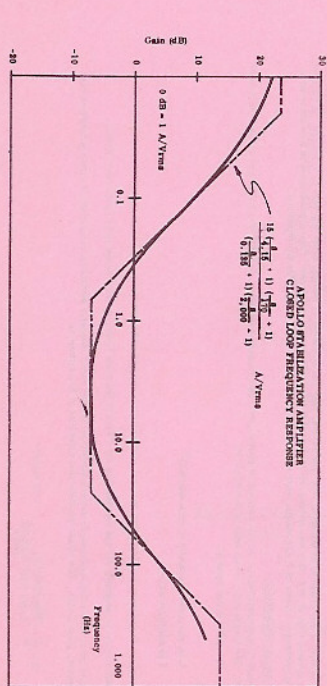
HW/23

IMU "INERTIAL MODE" FUNCTIONAL BLOCK DIAGRAM

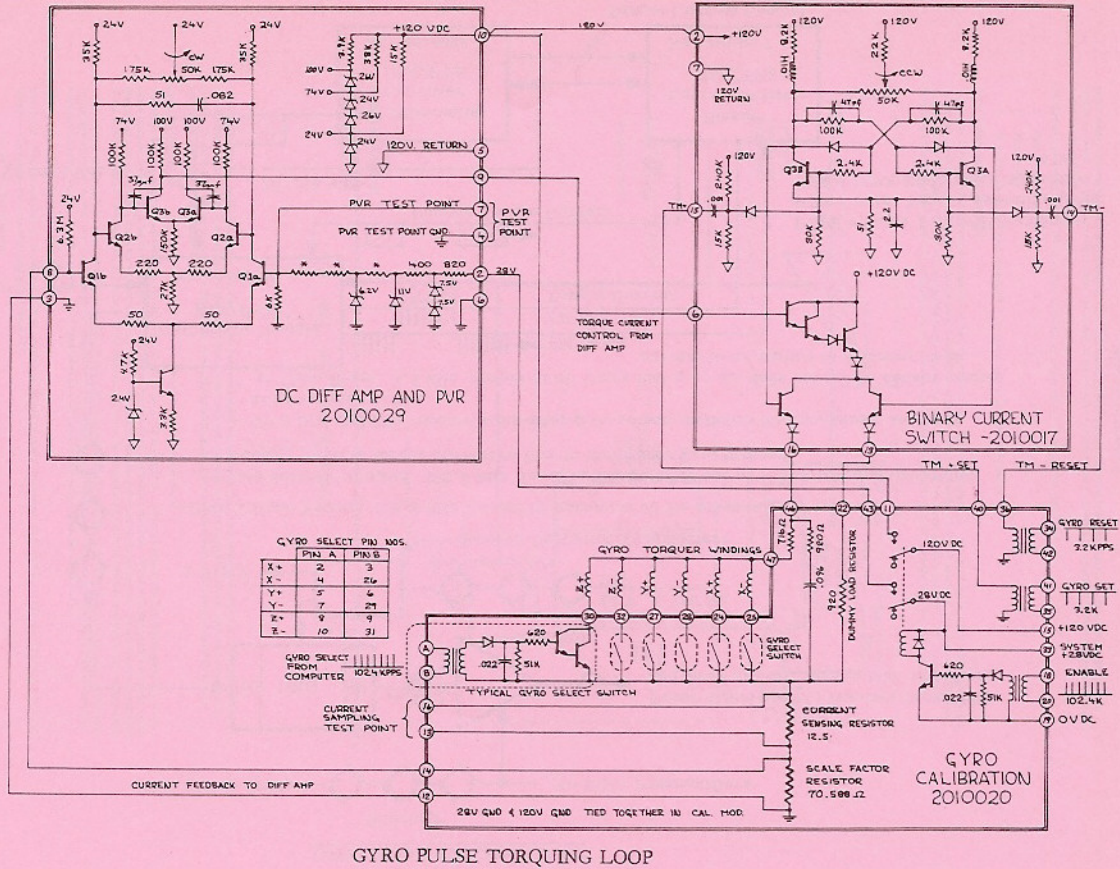


HW-24

$K_T = 1.52 \times 10^7 \frac{\text{dyn-cm}}{\text{A}}, I$	$K = 1 \frac{\text{A}}{\text{V rms}}, I$	$H = 0.434 \times 10^6 \frac{\text{dyn-cm}}{\text{rad/sec}}$
$1.52 \times 10^7 \frac{\text{dyn-cm}}{\text{A}}, M$	$2 \frac{\text{A}}{\text{V rms}}, M$	$C = 0.475 \times 10^6 \frac{\text{dyn-cm}}{\text{rad/sec}}$
$1.23 \times 10^7 \frac{\text{dyn-cm}}{\text{A}}, O$	$3 \frac{\text{A}}{\text{V rms}}, O$	$I = 367 \text{ gm-cm}^2$
		$H/C = 0.915$
		$C/I = 1,290 \text{ rad/sec}$

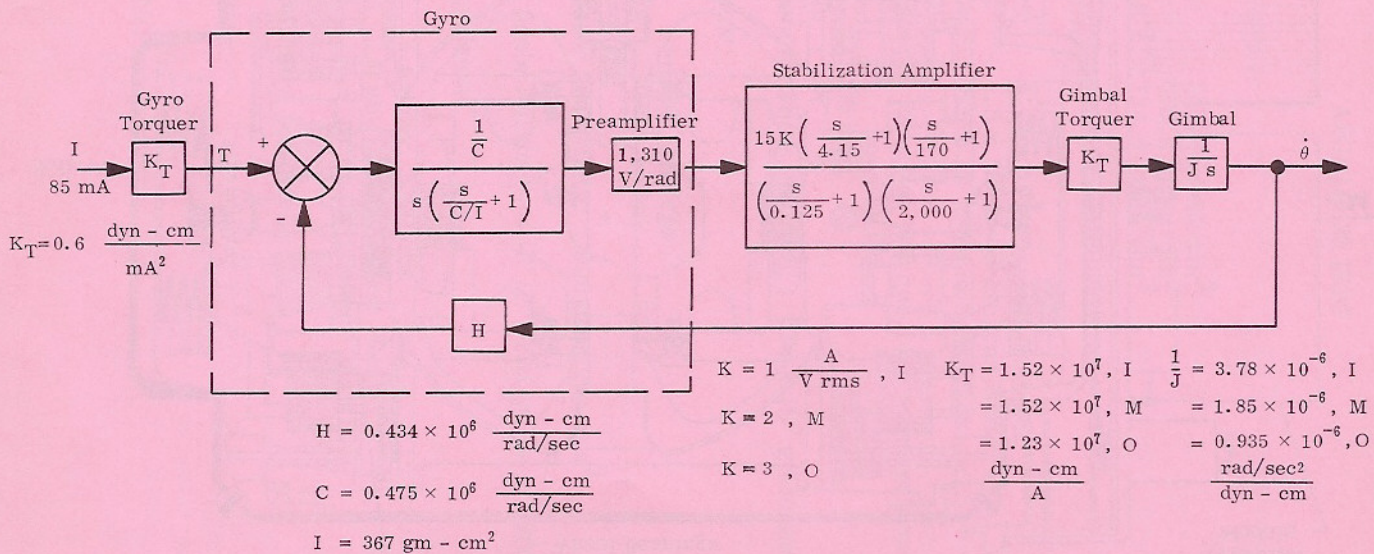


HW-25



GYRO PULSE TORQUING LOOP

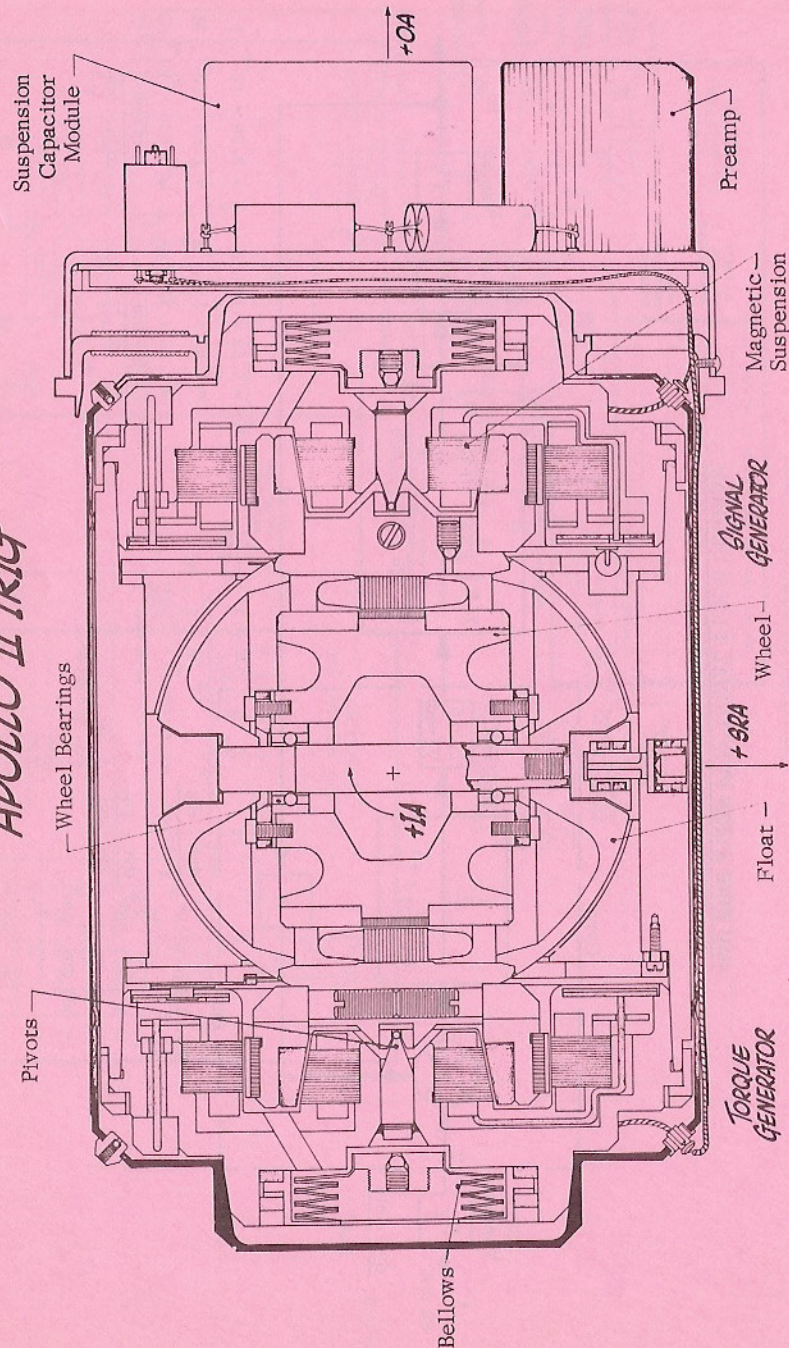
IMU FINE ALIGN FUNCTIONAL BLOCK DIAGRAM



Gyro Torque = $K_T I^2 = 0.6(85)^2 = 4,335 \text{ dyn-cm}$

APOLLO 25 IRIG

APOLLO II IRIG



Moments of Inertia:

about OA:	367.3 gram-cm ²
about IA:	650.8 gram-cm ²
about SRA:	724.9 gram-cm ²

Damping Coefficients:

about OA:	4.75 x 10 ⁵ dyne-cm/rad/sec
about IA:	1.5 x 10 ⁹ dyne-cm/rad/sec
about SRA:	1.5 x 10 ⁹ dyne-cm/rad/sec

Wheel Excitation: 28 volts, 800 cps, 4.5 watts at synchronism

Wheel Speed: 24,000 rpm

Angular Momentum at 24,000 rpm: 434 x 10³ gram-cm² sec

Signal Generator:

Input:	4 volts, 3200 cps
Sensitivity:	10 mv/mrad

Torque Generator Sensitivity: 0.6 dyne-cm/ma²

Pulse Torque Scale Factor: $\pi/2^{20}$ rad/pulse at 3200 pps

Magnetic Suspension:

Input:	4 volts, 3200 cps
Stiffness:	6 gm/0.0001 inches Radial
	0.8 gm/0.0001 inches Axial

Typical Temperature Sensitivity

Scale Factor:	400 ppm/ ^o F
Drift:	0.2 meru/ ^o F

Actual IRIG temperature

GYRO PARAMETERS

Primary gyro parameters are ADIA, ADIRA, HED and scale factor. Specification values across ISS, OAS, and S/C testing are as shown in Table II-1.

Table II-1
Gyro Coefficient Stability Criteria

Coefficient	Units	M	D2	D3	Max
Acceleration Drift along the IA (ADIA)	mu/r/s	17	33	40	100
Acceleration Drift along the SRA (ADIRA)	mu/r/s	14	21	25	40
Non-acceleration Bias Drift (HED)	mu/r/s	6	9	11	15

Gyro scale factor limits are ± 1790 ppm.

The maximum value of gyro performance parameters which can be compensated for by the computer is shown in Table II-2.

Table II-2

Coefficient	Units	Max Value CM/IM
ADIA	mu/r/s	862/630
ADIRA	mu/r/s	862/630
HED	mu/r/s	128.7

IMU GYRO COMPENSATION

The compensated PIPA data is used to compute the IRIG weighting necessary to cancel the HED, ADIA and ADIRA gyro coefficients. The computations are

$$\begin{aligned} XIRIG &= -ADIX PIPAV_G + ADIRAX PIPAV_G - HBIX \Delta t \\ YIRIG &= -ADIX PIPAV_G + ADIRAY PIPAV_G - HBIX \Delta t \\ ZIRIG &= -ADIZ PIPAV_G - ADIRAZ PIPAV_G + HBIZ \Delta t \end{aligned}$$

where

XIRIG, YIRIG, ZIRIG are gyro drift compensations

HBIX, HBIX, HBIZ are gyro bias drifts (an erasable load)

ADIRAX, ADIRAY, ADIRAZ are gyro drifts due to acceleration in spin reference axis (an erasable load)

ADIX, ADIY, ADIZ are gyro drifts due to acceleration in the input axis (an erasable load)

When the magnitude of any IRIG command exceeds two pulses, the commands are sent to the gyros. During free-fall only the HBIX, HBIX, HBIZ are the relevant coefficients and the routine is so ordered that only these terms are calculated for the gyro compensation.

The computer HED registers are 1460, 1461, and 1462 for the X, Y, and Z gyros respectively.

GYRO BIAS HED = (.007835) (bag Contents in Decimal) HBIX

HISTORY OF IN-FLIGHT GYRO DRIFT ERRORS



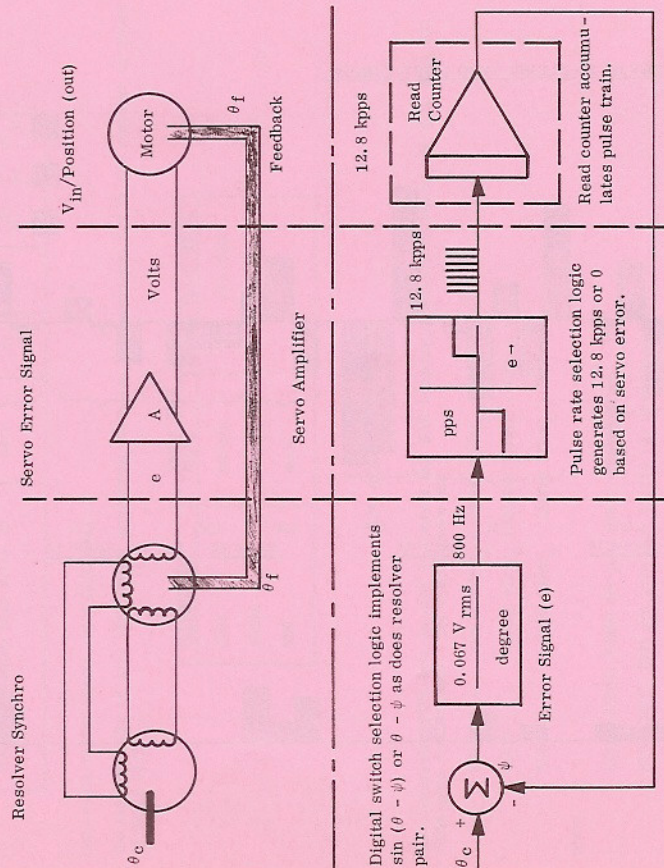
ELECTRONICS COUPLING DISPLAY UNIT (ECDU)

The ECDU encodes and scales the IMU gimbal angles and transfers the angles to the computer in the proper format.

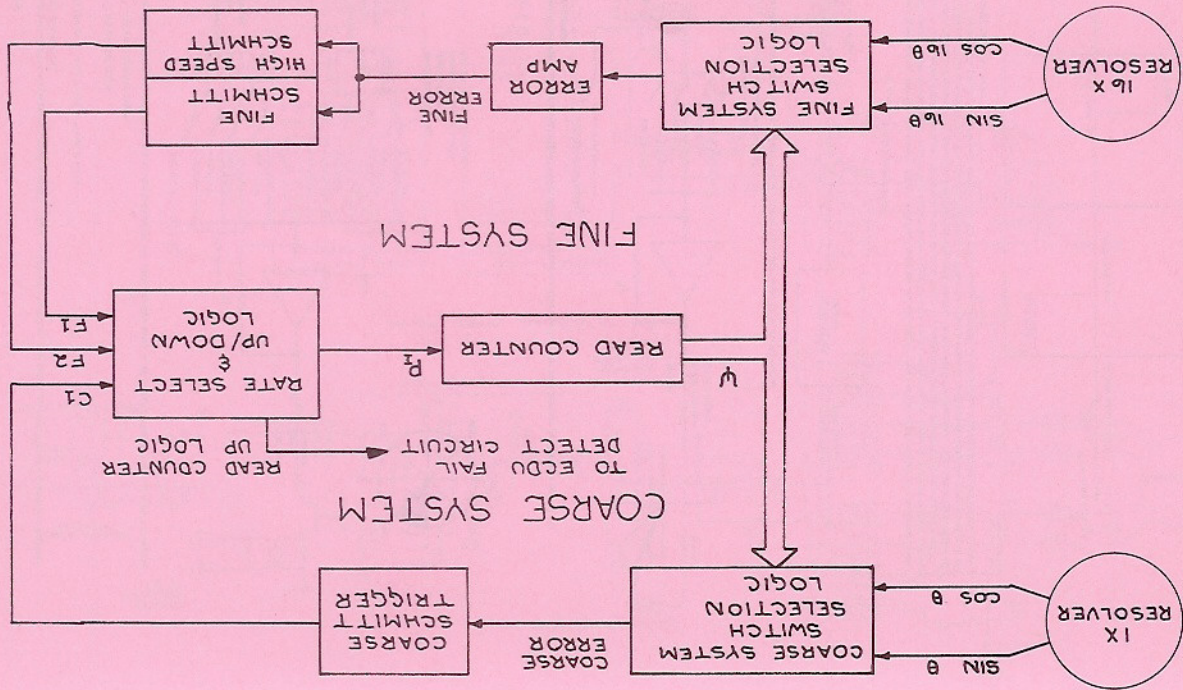
The ECDU is an analog-to-digital converter which utilizes two encoding loops and one read counter which can be accessed by the computer.

One encoding loop is used with the $16 \times$ gimbal resolver (the fine system); the other encoding loop is used with the $1 \times$ gimbal resolver (the coarse system).

The ECDU digital servo is analogous to a resolver synchro illustrated by the following diagram.



Digital switch selection logic implements $\sin(\theta - \psi)$ or $\theta - \psi$ as does resolver pair.



ECDU
READ COUNTER LOOP
BLOCK DIAGRAM

ELECTRONIC COMPUTING DISPLAY UNIT ECCU (CONTINUED)

The ECCU is an analog-digital converter which utilizes two switching loops and one read counter to which the computer has access. One loop is used to generate the coarse system's sine wave, the other, a "coarse system," encodes the coarse system's sine wave.

When the difference between a global resolver angle and the ECCU read counter exceeds 1.4 degrees, the "coarse system" has control of the read counter. For errors smaller than 1.4 degrees, the "fine system" has exclusive control of the read counter.

COARSE SYSTEM

The coarse system utilizes a system of switch selection logic, weighting resistors, and summing amplifiers to generate a trigonometric identity of the form:

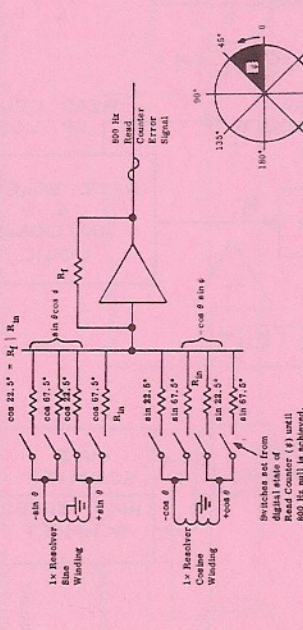
$$-\sin(\theta - \psi) = -\sin \theta \cos \psi + \cos \theta \sin \psi$$

where

θ = the global angle

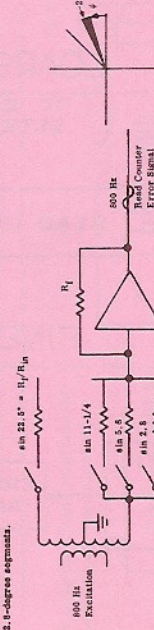
ψ = the read counter angle

This trigonometric identity is used as an error signal to drive the read counter until ψ has been isolated to one of eight 45-degree segments.

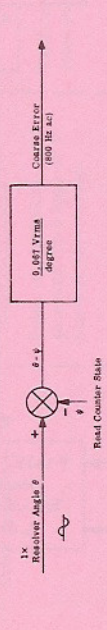


Switches set from digital state of Read Counter (ψ) until 600 Hz null is achieved.

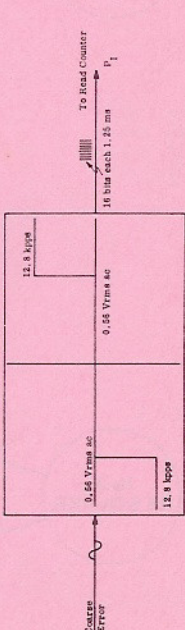
A ladder network provides an additional signal which drives the read counter until ψ has been further isolated to one of 16, 2.8-degree segments.



The functional equivalent of the entire coarse system error detecting logic is as shown below.



In the manner shown above, the error between the 1x resolver position and the digital state of the read counter is converted to an 800 Hz analog signal. This is converted back into a digital drive signal for the read counter by the "coarse Schmitt trigger and rate selection logic." The coarse Schmitt trigger threshold is set at 0.56 Vrms (0.4 degrees) thereby deactivating the coarse read counter loop when the error between 1x resolver position and the read counter is less than 0.4 degrees. The pulse train output of the Schmitt trigger is gated by a 1.600 pulses train. When the error is less than 0.4 degrees, the read counter is driven to a value of 13.8 bits and remains there until the error is less than 0.4 degrees. The functional equivalent of this signal processing is shown below.



16 bits \times 800 bursts/second \times 20 bits/burst = 10⁷ bits/second

Coarse Schmitt Trigger and Rate Selection Logic

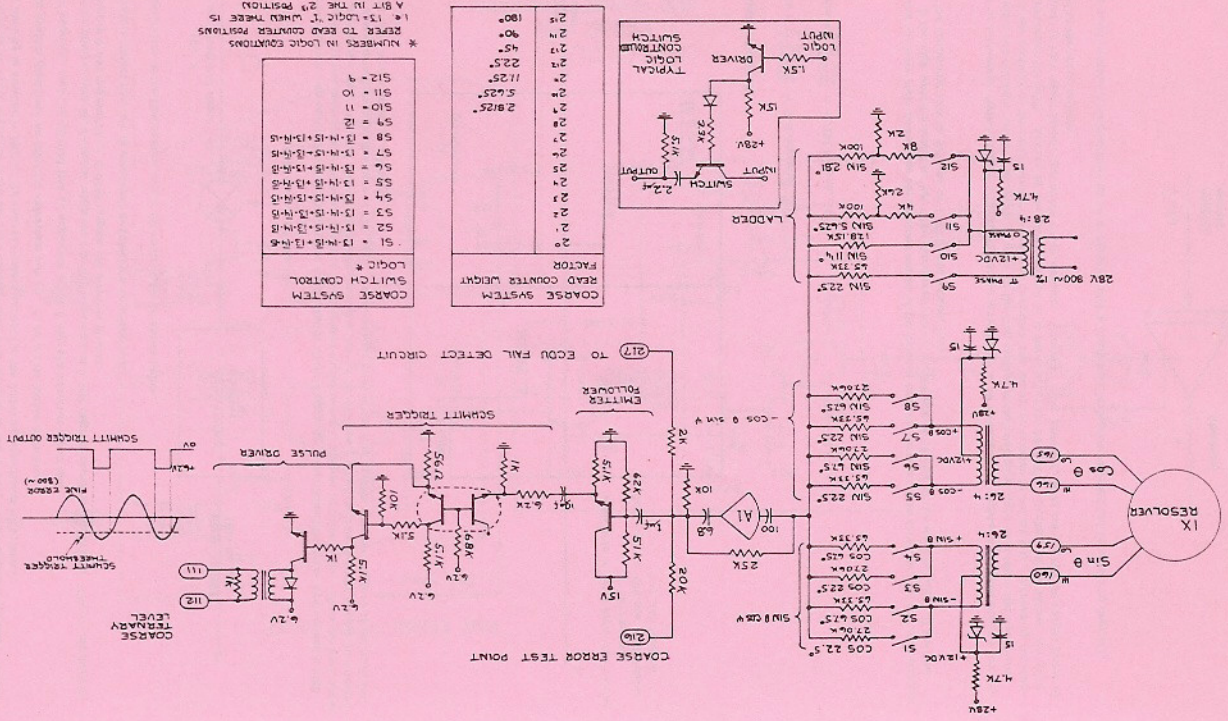
(The CM optica CDU's do not contain a coarse system.)

ECCU COARSE SYSTEM SWITCHING NETWORK

* NUMBERS IN LOGIC EQUATIONS REFER TO READ COUNTER POSITIONS 1 & 15 IN LOGIC 1 LINE THESE IS A BIT IN THE 2nd POSITION

COARSE SYSTEM SWITCH CONTROL LOGIC ψ
S1 = 13-N-15 + 13-N-15
S2 = 13-N-15 + 13-N-15
S3 = 13-N-15 + 13-N-15
S4 = 13-N-15 + 13-N-15
S5 = 13-N-15 + 13-N-15
S6 = 13-N-15 + 13-N-15
S7 = 13-N-15 + 13-N-15
S8 = 13-N-15 + 13-N-15
S9 = 12
S10 = 11
S11 = 10
S12 = 9

COARSE SYSTEM READ COUNTER WEIGHT FACTOR
2 @ 25°
5 @ 25°
11 @ 25°
22 @ 25°
45 @ 25°
90 @ 25°
180°



ELECTRONIC COUPLING DISPLAY UNIT (ECDU) (CONTINUED)

Fine System

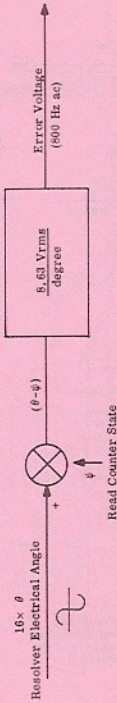
The fine system utilizes a system of switch selection logic, weighting resistors, and summing amplifiers to generate the trigonometric identity.

$$-\sin(\theta - \psi) = \sin \theta \cos \psi + \cos \theta \sin \psi$$

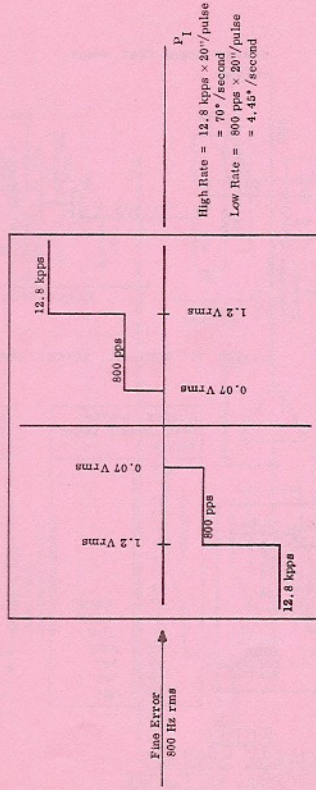
This identity is used as an error signal to drive the read counter until ψ is isolated to one of 256, 0.088-degree segments. Mechanization of this identity is similar to that of the coarse system except for the following significant differences.

1. The fine system generates an internal reference $\cos(\theta - \psi)$ to excite ladder switches.
2. The 16x resolver angle is divided into 16 segments of 22.5 degrees resolver electrical.
3. The ladder divides each 22.5-degree segment into 256 segments of 0.088 degree resolver electrical. The 1x mechanical equivalent is 0.088 degree/16 or 0.0055 degree (29 seconds of arc).
4. A quadrature reject network is employed to achieve a true loop null.

The functional equivalent of the fine system error detection logic is shown below.



Fine systems error signals are converted back into digital read counter drive signals by a fine Schmitt trigger system and a high speed Schmitt trigger system operating in tandem. Each is interrogated by the same 1,600 pps impulse train. When the high speed Schmitt trigger is detected high, the read counter is incremented at a rate of 12,800 pps. When the fine Schmitt trigger is detected high, the read counter is incremented at a rate of 800 pps. The threshold level of the fine system Schmitt triggers is set such that the rate selection logic shown below is achieved.



Schmitt Trigger Threshold

$$1.2 \text{ Vrms} \times \frac{1 \text{ degree}}{5.63 \text{ Vrms}} = 2.2 \text{ degrees}, 16x$$

$$0.07 \text{ Vrms} \times \frac{1 \text{ degree}}{5.63 \text{ Vrms}} = 0.131 \text{ degree}, 16x$$

The rate select and up/down logic processes the sampled output of the Schmitt triggers to determine at which rate the read counter is incremented. If the high speed Schmitt is "on," the increment rate is 12,800 pps or 16 bits in one sample time. If the fine Schmitt is "on," the increment rate is 800 pps or 1 bit in one sample time.

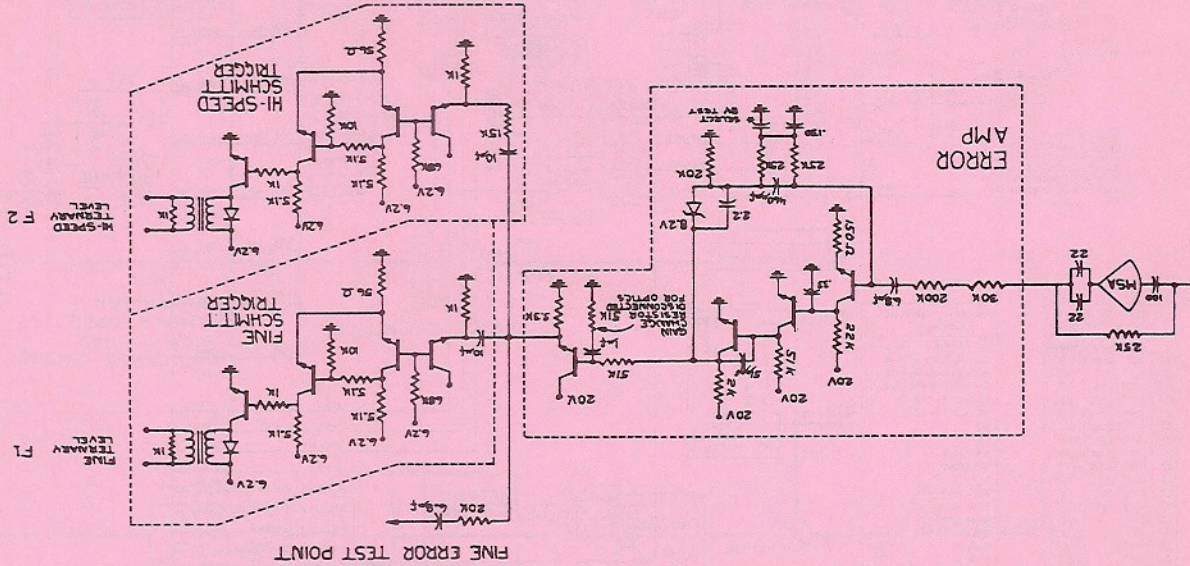
Read Counter

The read counter is a 16-stage digital counter with an IMU angle granularity of 20 seconds/bit. Inputs to the read counter are controlled by rate select and up/down logic. The output is used by the fine system switch selection logic to produce the fine error signal. Access to the contents of the read counter by both the computer and the error counter is serial only. Access by the computer is to the first order bit ($\Delta\theta^2$). Access by the error counter is to the third order bit ($\Delta\theta^3$). Because of the above, the count rate to the computer is four times faster than to the error counter. Computer bits, however, have one-fourth the weight factor of those transferred to the error counter.

Functionally, the read counter is represented as an integrator with a count rate in degrees/second and an output in degrees.

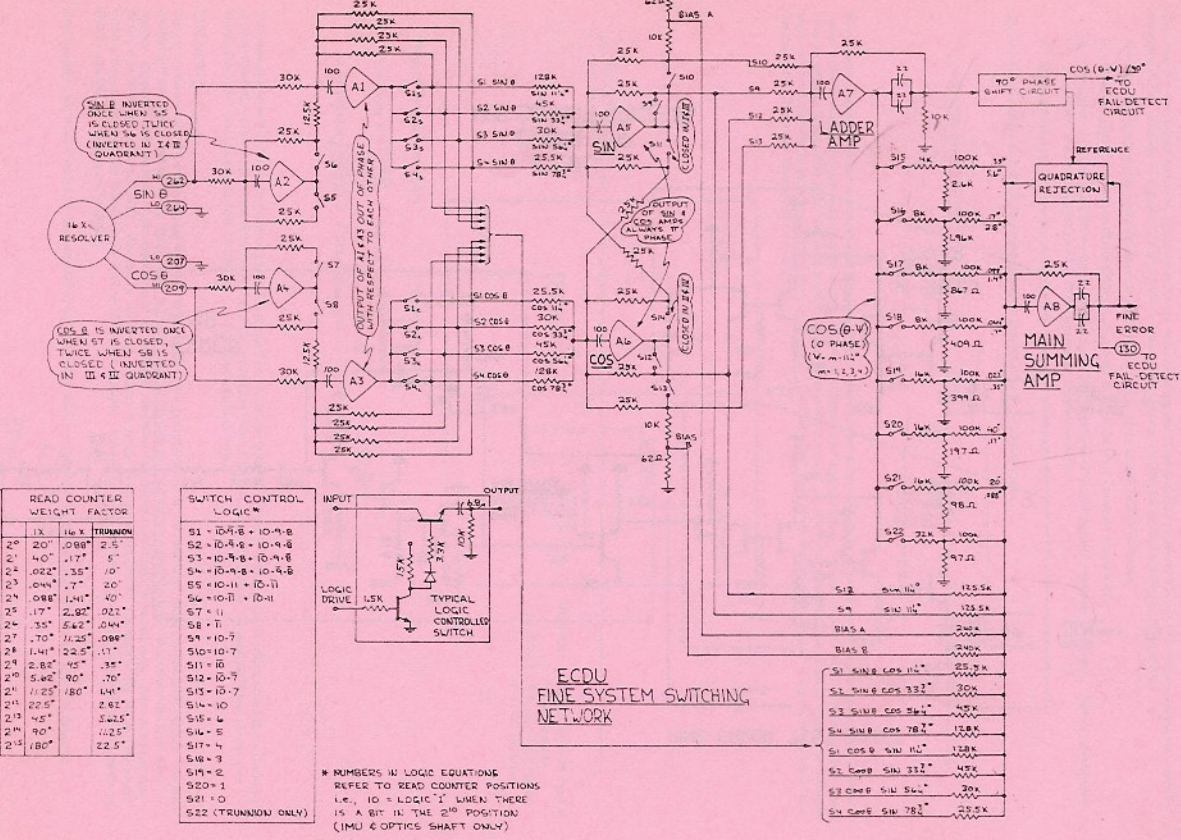


ECDU ERROR AMP AND SCHMITT TRIGGERS



FINE ERROR TEST POINT

ERROR AMP

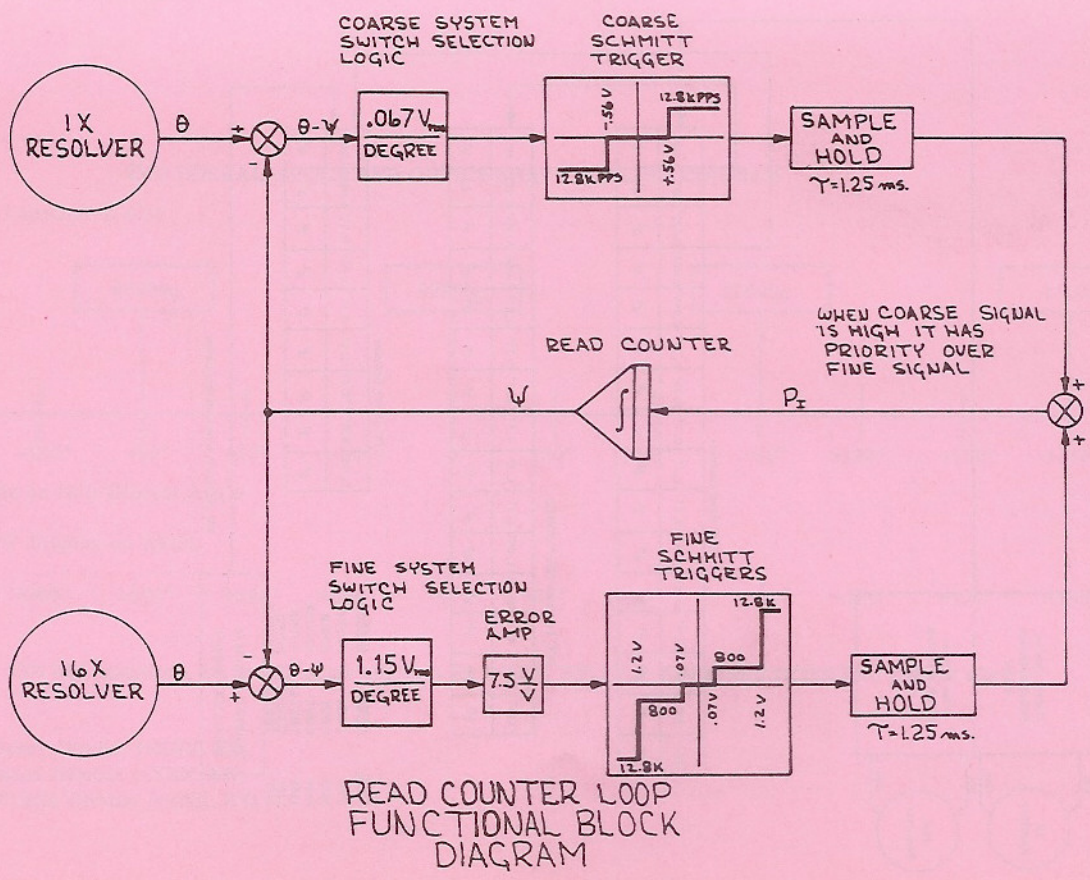


1x	16x	TRUNCATION
2 ⁰	20°	.088° 2.5°
2 ¹	40°	.17° 5°
2 ²	80°	.35° 10°
2 ³	160°	.7° 20°
2 ⁴	320°	1.4° 40°
2 ⁵	640°	2.8° 80°
2 ⁶	1280°	5.6° 160°
2 ⁷	2560°	11.2° 320°
2 ⁸	5120°	22.5° 640°
2 ⁹	10240°	45° 1280°
2 ¹⁰	20480°	90° 2560°
2 ¹¹	40960°	180° 5120°
2 ¹²	81920°	360° 10240°
2 ¹³	163840°	720° 20480°
2 ¹⁴	327680°	1440° 40960°
2 ¹⁵	655360°	2880° 81920°

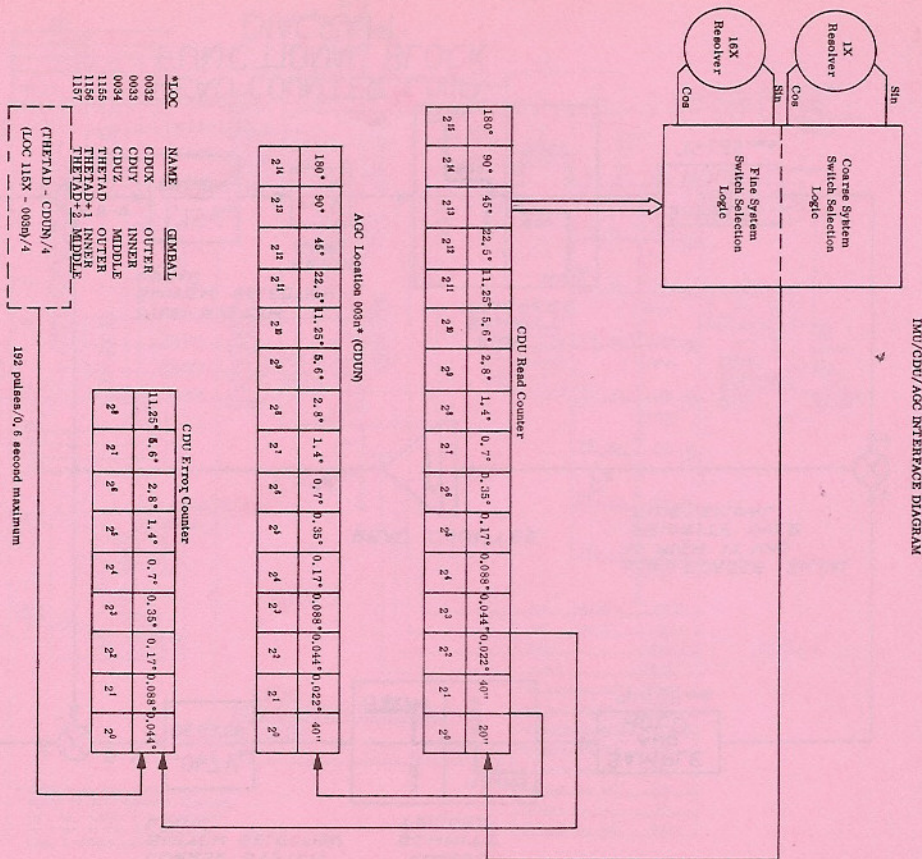
SWITCH CONTROL LOGIC*

S1 = 10-9-B + 10-9-B
 S2 = 10-9-B + 10-9-B
 S3 = 10-9-B + 10-9-B
 S4 = 10-9-B + 10-9-B
 S5 = 10-11 + 10-11
 S6 = 10-11 + 10-11
 S7 = 1
 S8 = 1
 S9 = 10-7
 S10 = 10-7
 S11 = 10
 S12 = 10-7
 S13 = 10-7
 S14 = 10
 S15 = 4
 S16 = 5
 S17 = 4
 S18 = 3
 S19 = 2
 S20 = 1
 S21 = 0
 S22 (TRUNCATION ONLY)

* NUMBERS IN LOGIC EQUATIONS REFER TO READ COUNTER POSITIONS. I.E., 10 = LOGIC "1" WHEN THERE IS A BIT IN THE 10TH POSITION (1M & OPTICS SHAFT ONLY).

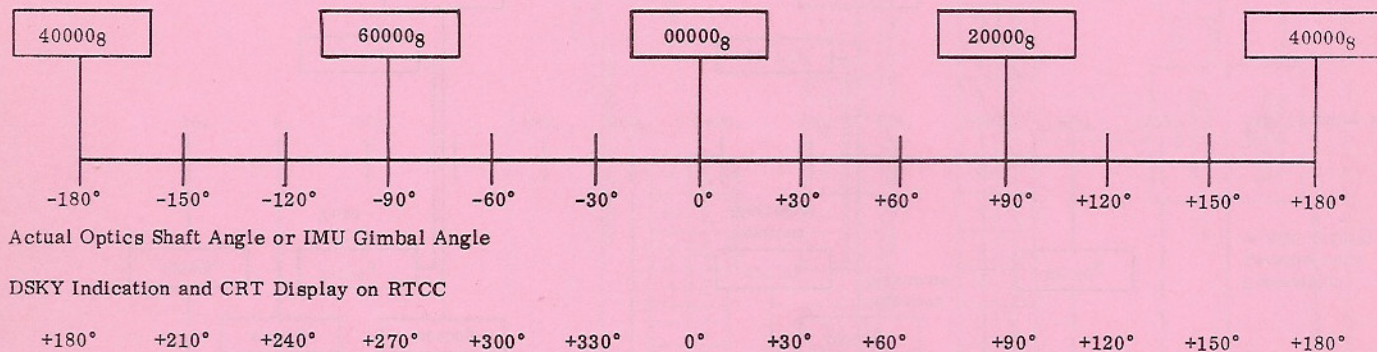


READ COUNTER LOOP FUNCTIONAL BLOCK DIAGRAM



IMU GIMBAL ANGLE AND OPTICS SHAFT SCALING DIAGRAM

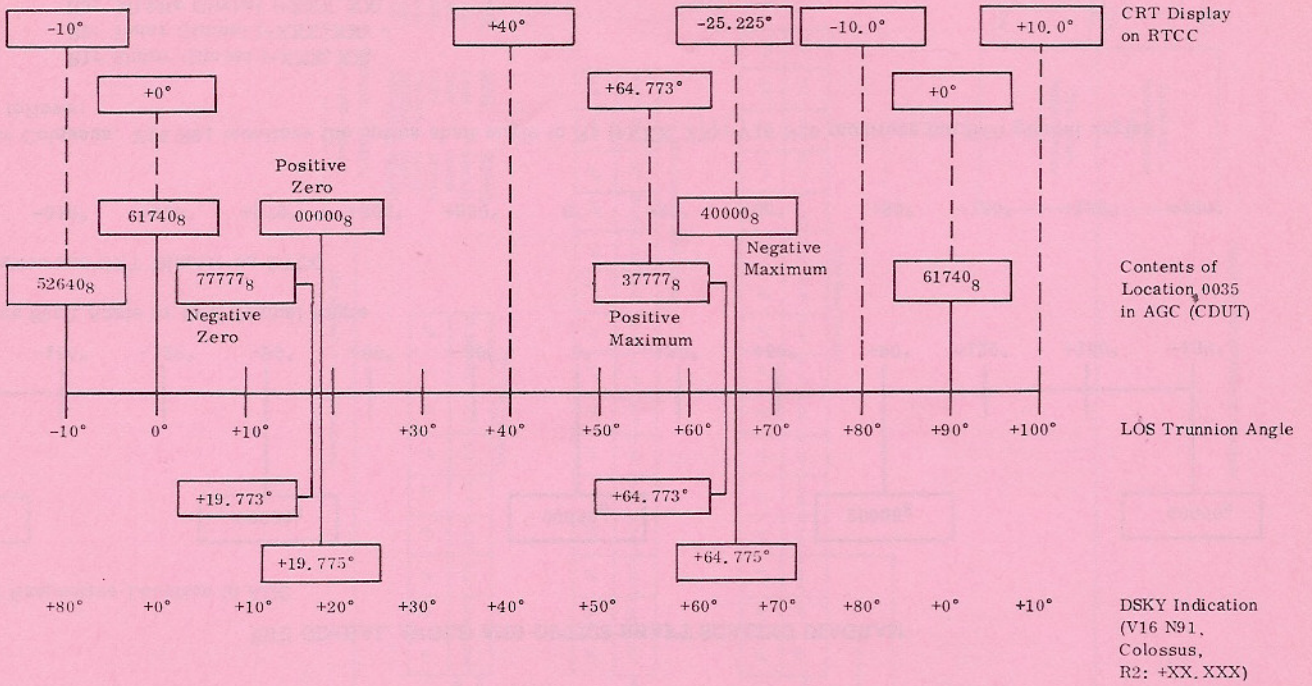
Contents of Respective Location in AGC



NOTE: For Colossus, V16 N91 monitors the optics shaft angle in R1 (+XXX.XX); V16 N20 monitors the IMU gimbal angles as follows:

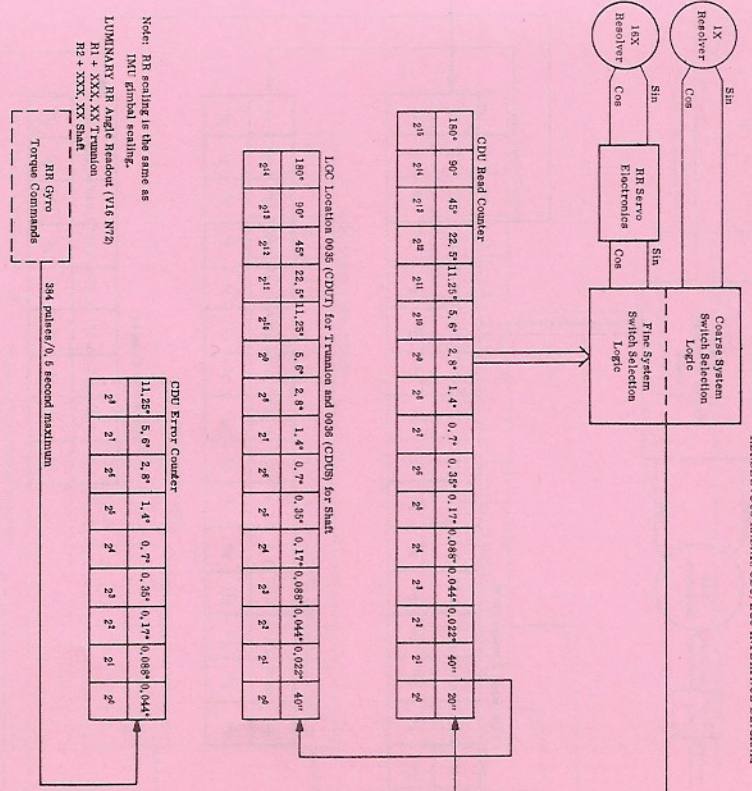
- R1: Outer Gimbal (+XXX.XX)
- R2: Inner Gimbal (+XXX.XX)
- R3: Middle Gimbal (+XXX.XX)

OPTICS TRUNNION SCALING DIAGRAM



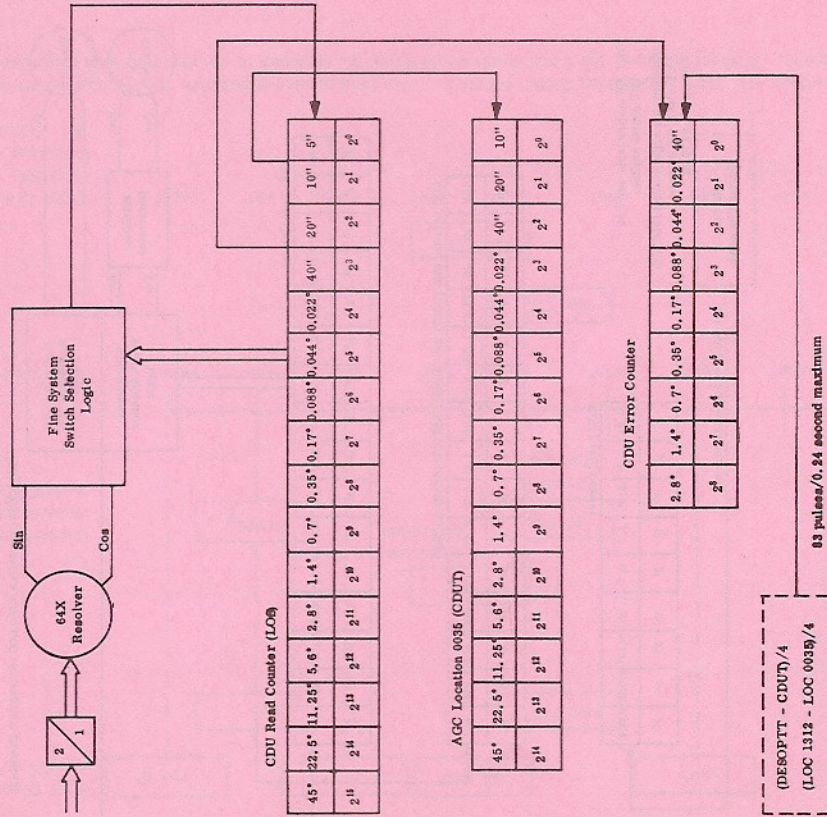
HW-44

NOTE: CDUT (LOC 0035) is loaded with a -19.775° bias during ZERO OPTICS. This bias produces positive driving commands for angles up to +64.775° in the CMC mode. Without the bias, the CMC mode would drive the trunnion to the negative stop for command angles of 45° or greater.

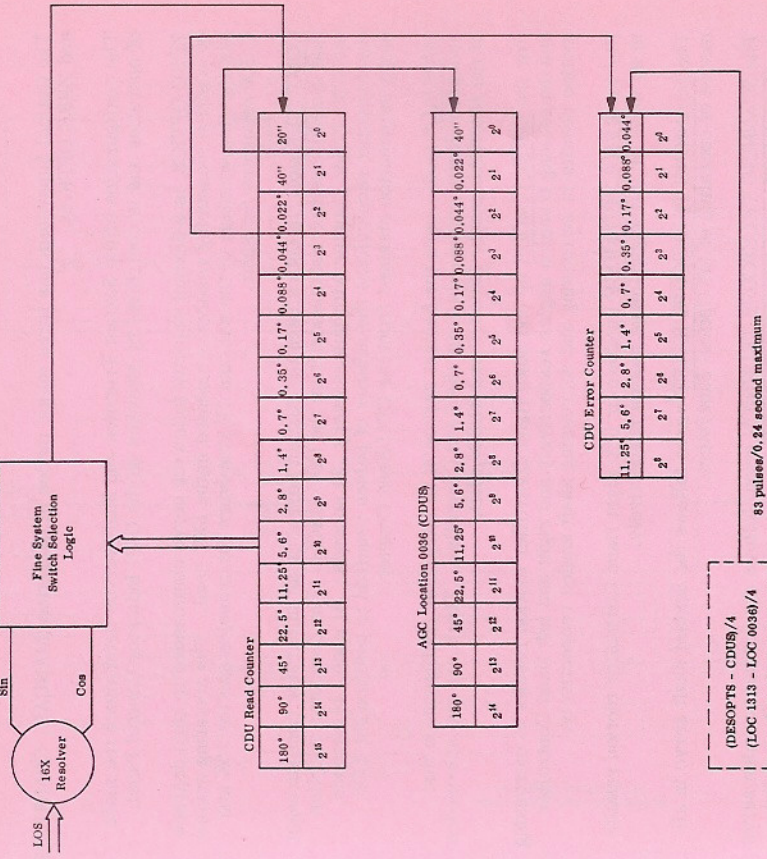


HW-45

OPTICS TRUNNION/CDU/AGC INTERFACE DIAGRAM



OPTICS SHAFT/CDU/AGC INTERFACE DIAGRAM



CM OPTICS

The Optical Subsystem has three major modes of operation (MANUAL, COMPUTER, and ZERO OPTICS).

The configuration of the Sextant Trunnion and Shaft servos determines the mode of operation and is controlled by switches on the G&N Indicator Control Panel.

ZERO OPTICS is a Sextant velocity follow-up servo commanded to zero degrees by position measuring resolvers mounted in the SXT head. The actuating errors are the sine windings of the 64X and 1X resolvers for trunnion and the 16X and 1/2 X resolvers for shaft.

COMPUTER operate optics uses the Sextant servos in an integrator configuration that accepts position commands from the CDU DAC's. The computer supplies the CDU Error Counter with pulses every .24 seconds. At each .24 seconds, the computer recalculates the number of pulses required to position the LOS using information obtained from the CDU Read Counter.

MANUAL* mode uses the Sextant servos in an integrator configuration that accepts velocity commands from the Hand Controller. There are two sub-modes of the MANUAL mode, DIRECT and RESOLVED.

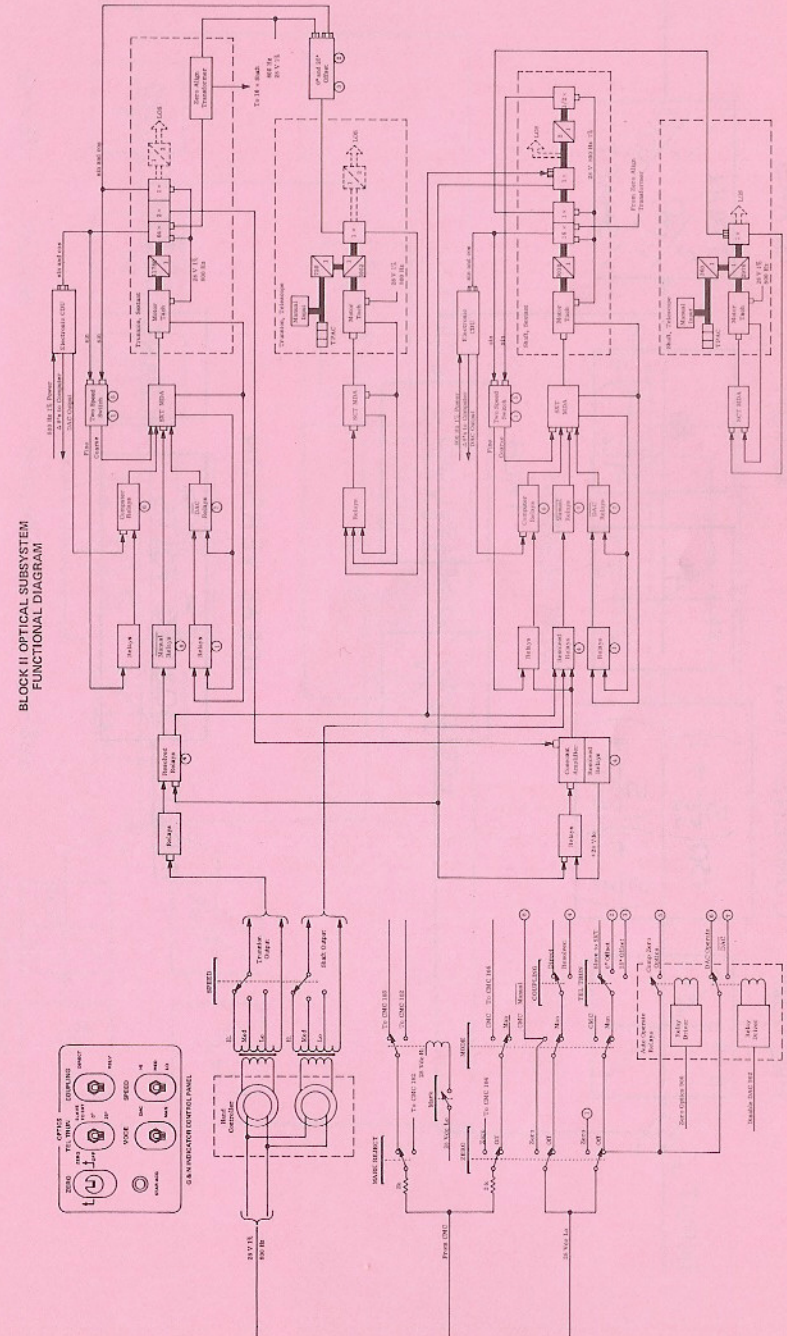
For the **DIRECT MODE** up and down Hand Controller motion results in increasing and decreasing trunnion angles respectively and right and left Hand Controller motion results in increasing and decreasing shaft angles respectively.

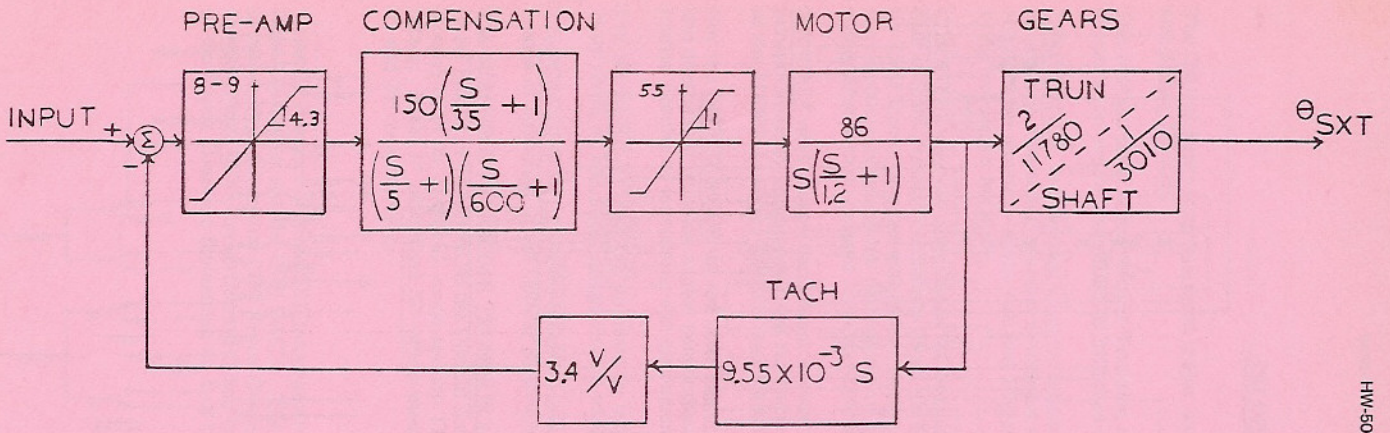
For the **RESOLVED MODE** up-down or left-right Hand Controller motion results in up-down or left-right image motion respectively.

The **SCANNING TELESCOPE SHAFT** servo follows the Sextant shaft servo in all modes of operation of the Optical Subsystem.

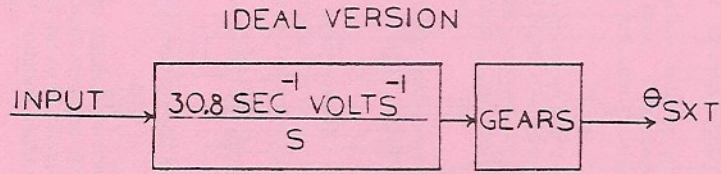
The **SCANNING TELESCOPE TRUNNION** servo follows the Sextant trunnion servo in all modes of operation of the Optical Subsystem except when the Optical Subsystem is in the MANUAL mode and the TEL. TRUN is set to 0° offset and 25° offset. In the 0° offset and 25° offset modes, the SCT trunnion angle is held at 0° and 25° respectively.

* During Program 24, the SXT integrators accept velocity commands from the Hand Controller and the CDU DAC's. The computer supplies rate commands to the shaft and trunnion CDU Error Counters (by setting bit 2 of Channel 12, Enable Optics CDU Error Counters) and inhibits feedback from the Read Counters to the Error Counters (by setting bit 8 of Channel 12, TVC Enable).



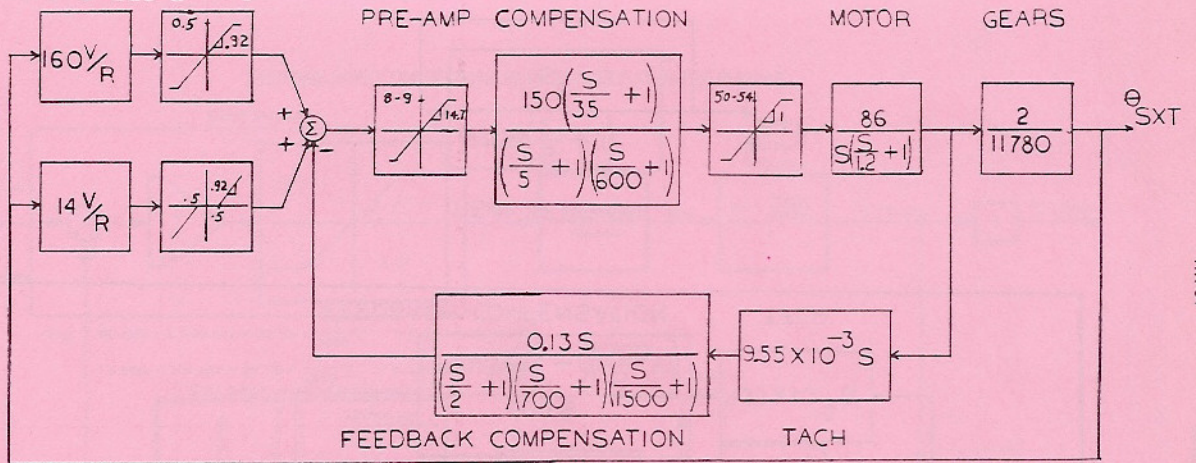


HW-50

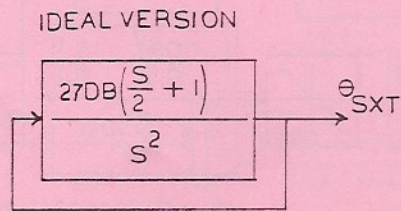


SXT INTEGRATORS

RESOLVERS AND TWO SPEED SWITCH

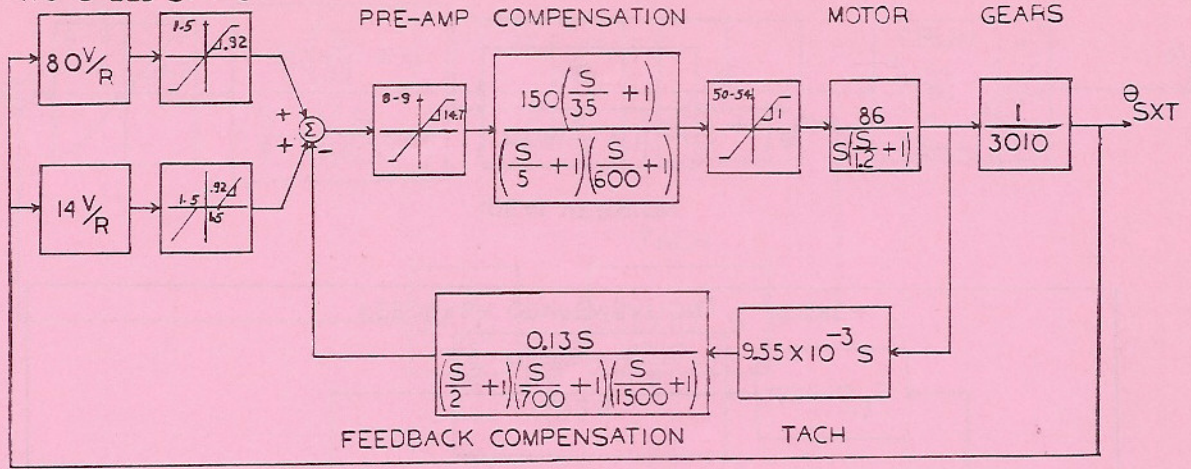


HW-51



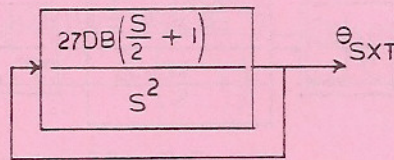
TRUNNION ZERO OPTICS SERVO

RESOLVERS AND TWO SPEED SWITCH



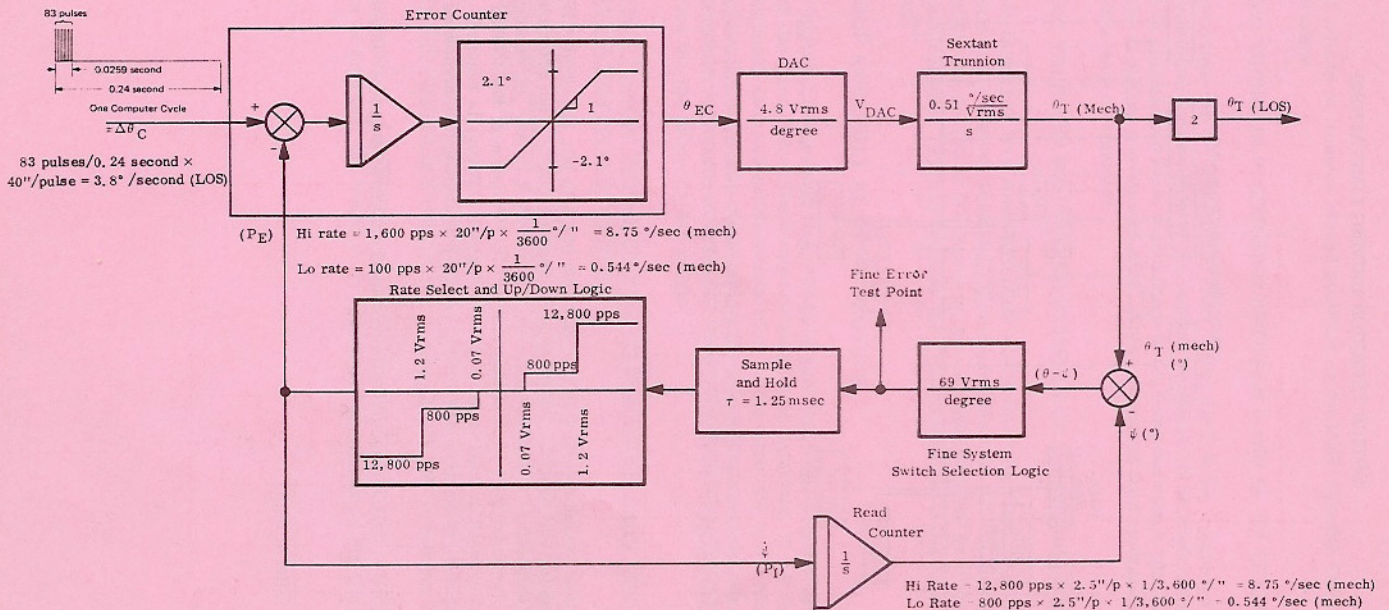
HW-52

IDEAL VERSION



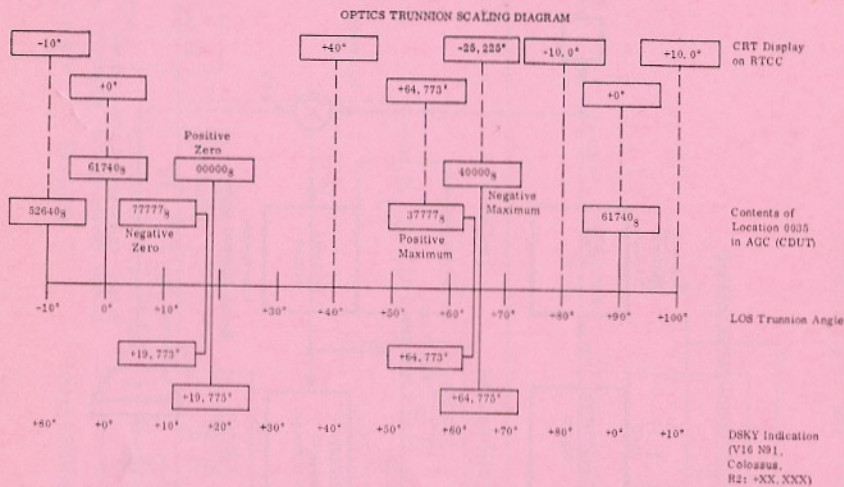
SHAFT ZERO OPTICS SERVO

TRUNNION COMPUTER OPERATE MODE FUNCTIONAL BLOCK DIAGRAM



HW-53

OPTICS TRUNNION DRIVING LIMITATIONS



NOTE: CDUT (LOC 0035) is loaded with a $-19,775^\circ$ bias during ZERO OPTICS. This bias produces positive driving commands for angles up to $+64,775^\circ$ in the CMC mode. Without the bias, the CMC mode would drive the trunnion to the negative stop for command angles of 45° or greater.

Since optics trunnion scaling is 9.88 arc-sec per data bit, the 14-bit CDUT register in the Computer will overflow (40000_g) at 45° LOS*. This overflow is interpreted by the Computer as a negative angle when in reality it is not. By biasing the CDUT location (register 035) in the Computer by $-19,775^\circ$, the counter (035) will not overflow until an LOS angle of $64,775^\circ$ (40000_g) is reached. In this case, the Computer (optics trunnion driving routine) interprets an overflow as $-45,000$ degrees $+19,775$ degrees or $-25,225$ degrees.

The maximum usable trunnion angle is approximately 57° . Beyond this angle the line of sight is completely vignettted.

*LOS = Line of Sight

The operational effects of trunnion biased overflow on the CMC Operate Optics mode is summarized below.

1. OPTICS CMC TRUNNION POSITIONING BETWEEN 0 AND $64,773$ DEGREES

Any angle in this range can be commanded from within the 0 to $64,7$ -degree range and the Computer will drive the trunnion to the correct position. If an angle greater than $64,773$ degrees is commanded from within the 0 to $64,7$ -degree range, the trunnion will be driven into the 0-degree mechanical stop. This is because the Computer thinks a negative angle has been commanded.

2. OPTICS CMC TRUNNION POSITIONING BETWEEN $64,775$ AND 90 DEGREES

Any angle in this range can be commanded from within this range and the Computer will drive the trunnion to the correct position. If an angle less than $64,775$ degrees is commanded from within the $64,775$ to 90 -degree range, the trunnion will be driven into the 90 -degree mechanical stop. This is because the Computer thinks a positive angle has been commanded.

3. OPTICS LOOP AMBIGUITY AT $64,775$ DEGREES

The Computer Operate Optics mode is a multiloop position followup servo operated under control of the Computer. The Computer supplies a maximum of 83 command pulses ($0,92$ degrees) each 240 milliseconds (ms) in the form of a 3,200 pps burst. The response of the Optics is then measured by the Computer once each 240 ms and appropriate correction commands are issued. This sample data loop has an ambiguity at $64,775$ degrees for the following two cases.

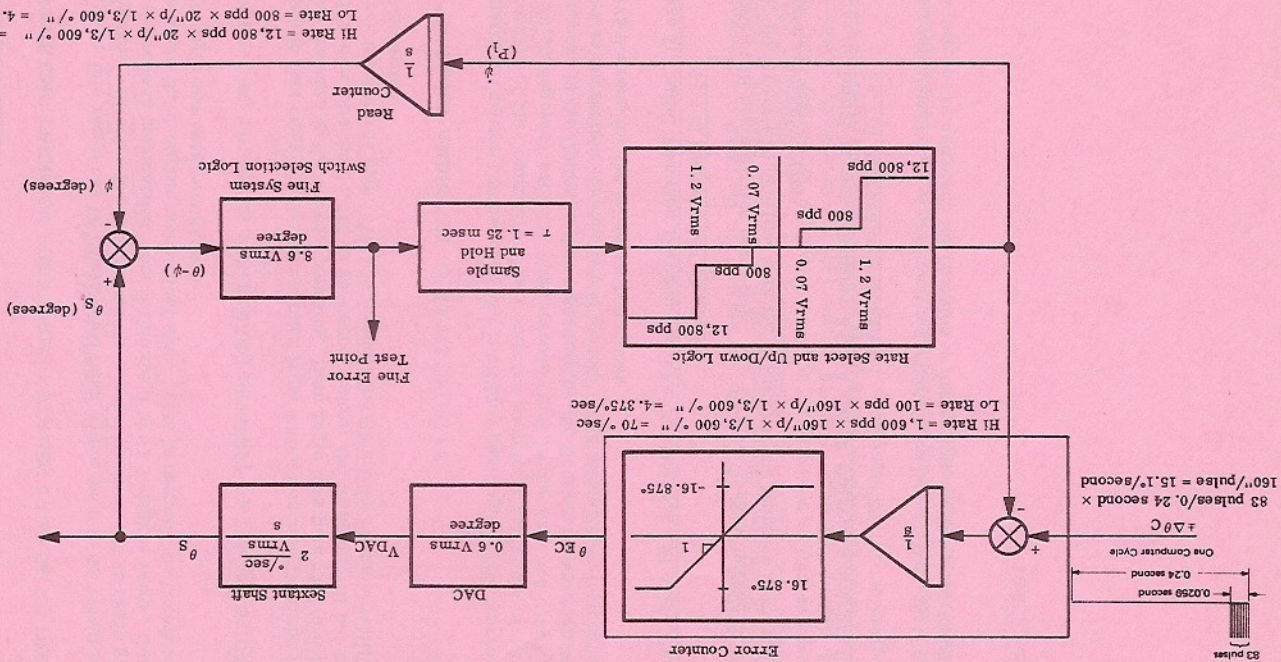
a. Case 1: Initial Trunnion Between 0 and $64,773$ Degrees and Angle Close to $64,773$ Degrees is Commanded

The Optics will begin driving correctly. If a servo overshoot beyond $64,773$ degrees occurs, the CDUT will overflow with the result that the trunnion is driven into the 90 -degree mechanical stop.

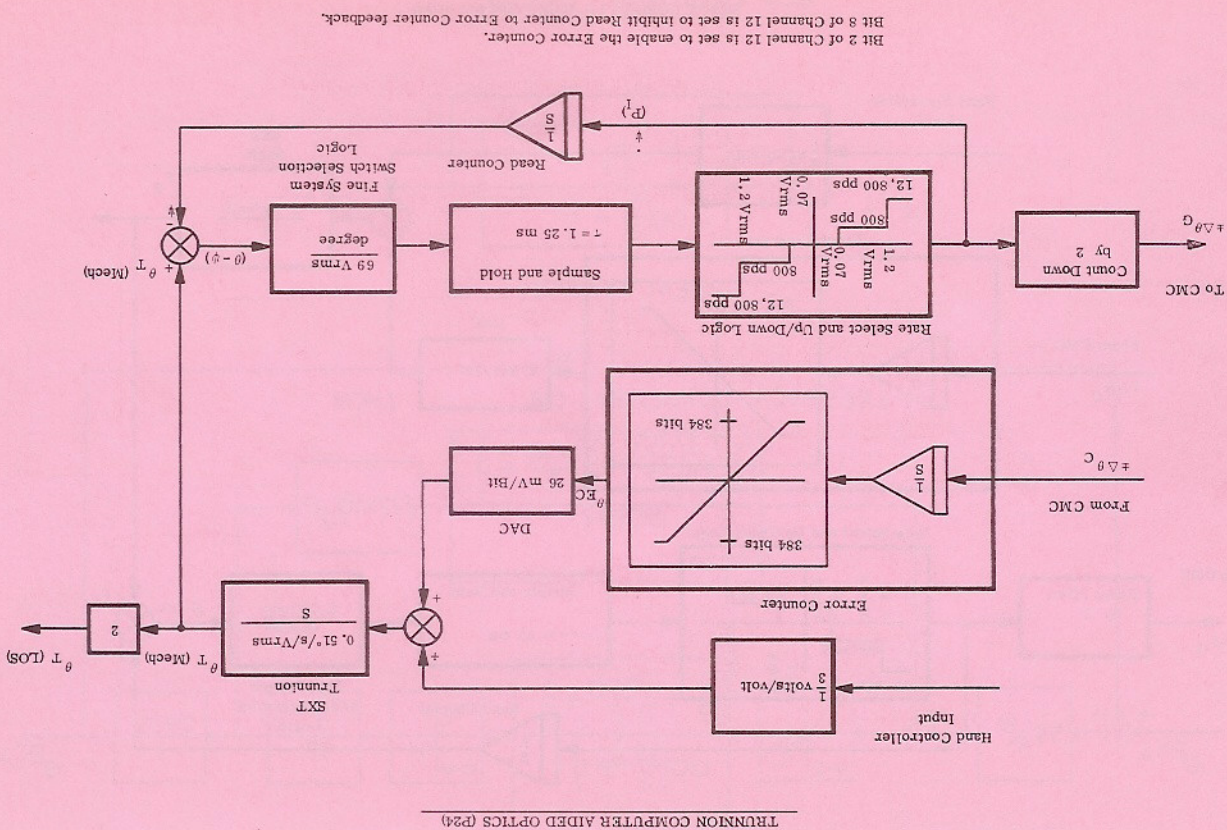
b. Case 2: Initial Trunnion Between $64,775$ and 90 Degrees and Angle Close to $64,775$ Degrees is Commanded

If the servo overshoot is of sufficient magnitude such that a CDUT angle of less than $64,775$ degrees is detected, the trunnion is driven into the 0-degree mechanical stop.

SHAFT COMPUTER OPERATE MODE FUNCTIONAL BLOCK DIAGRAM

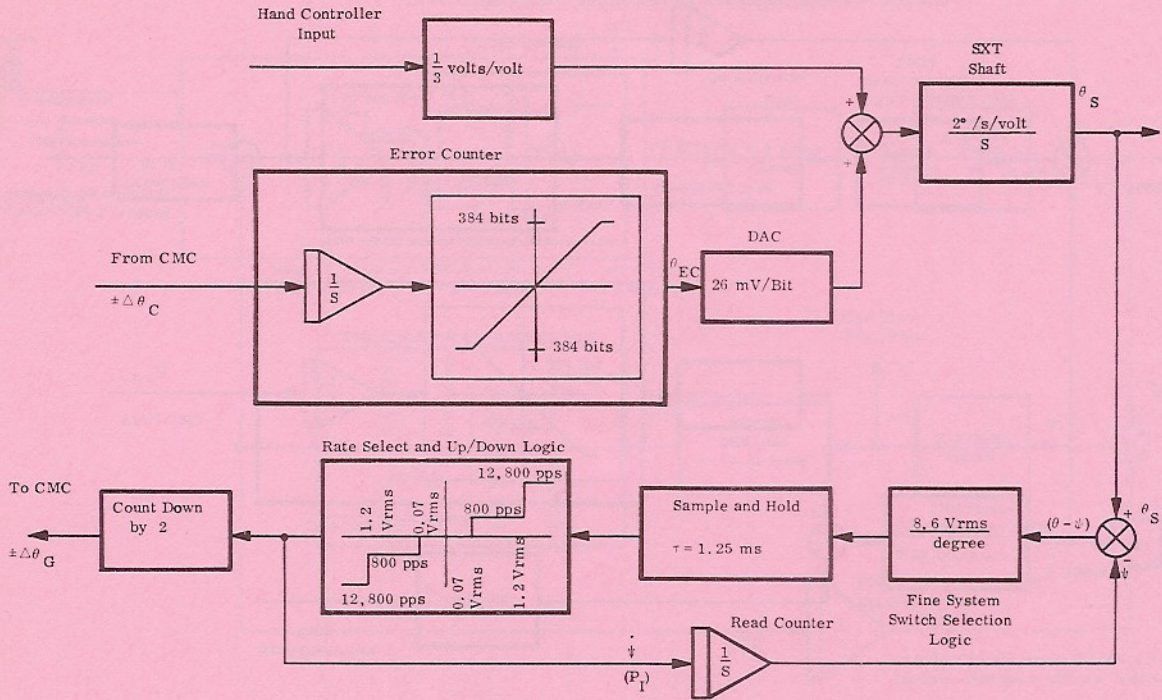


HW-56

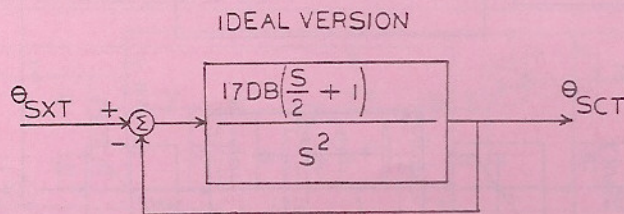
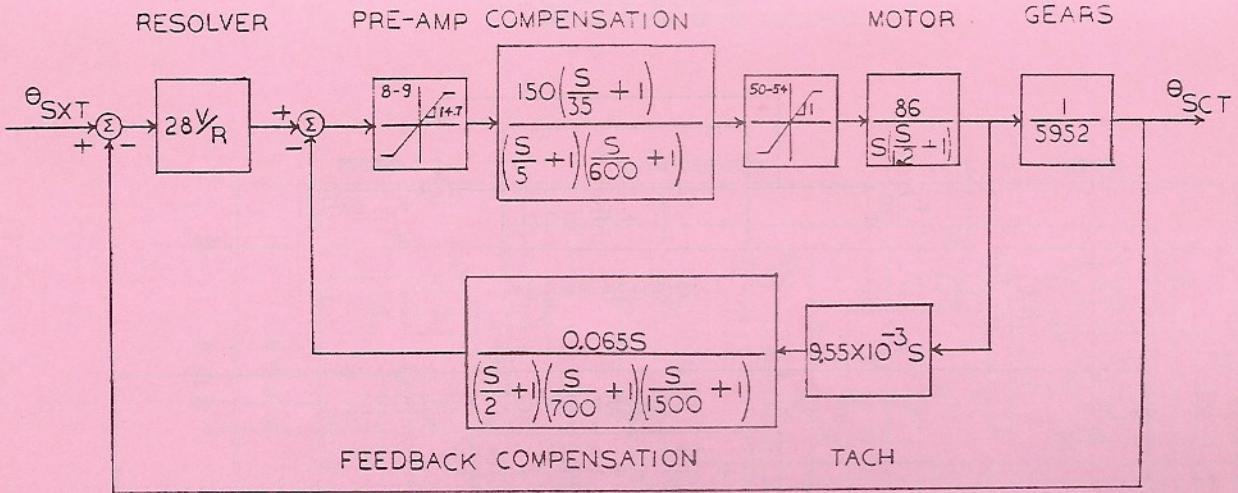


TRUNNON COMPUTER AIDED OPTICS (P24)

HW-57



Bit 2 of Channel 12 is set to enable the Error Counter.
 Bit 8 of Channel 12 is set to inhibit Read Counter to Error Counter feedback.



SCT TRUNNION SERVO

BLOCK II SERVO PARAMETERS

MOTOR SPEEDS VERSUS LOS RATES

TRUNNION	MOTOR SPEEDS		
	SMT	SXT	SOT
10.0 deg/sec	rpm 9817	rpm 4960	rpm 519
1.0 deg/sec	982	496	51.9
0.1 deg/sec	98	49.6	5.2
25.0 deg/sec	6.8	10.3	0.36
19.5 deg/sec	1064	0.71	1013
2.0 deg/sec	9870	105	104
0.2 deg/sec	1003	100	10.4
50.0 deg/sec	100	10.5	0.72
	7.0	0.73	

SERVO SENSITIVITY

SXT TRUNNION	CALCULATION	SENSITIVITY
Hand Controller	$105 \frac{mV}{mv} \times 205 \times \frac{2}{3.4}$	$1.08 \frac{deg/sec}{mv}$
Tachometer	3.4×1.08	$3.68 \frac{deg/sec}{mv}$
SMT SHAFT		
Hand Controller	$109 \frac{mV}{mv} \times 205 \times \frac{1}{3.4}$	$2.1 \frac{deg/sec}{mv}$
Tachometer	3.4×2.1	$7.14 \frac{deg/sec}{mv}$

MAXIMUM CREEP RATES DUE TO RESIDUALS

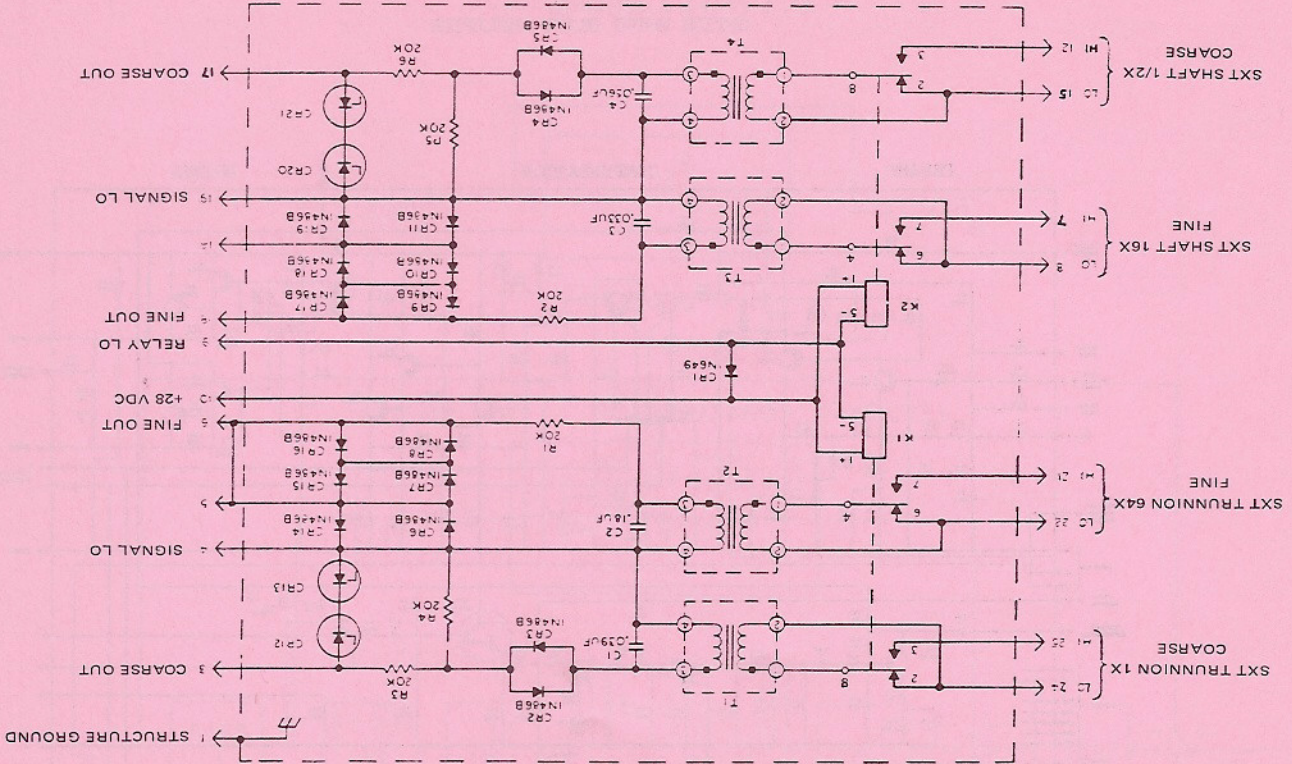
SXT TRUNNION	MAXIMUM RESIDUAL	SERVO SENSITIVITY	THEORETICAL MAXIMUM RATE
Hand Controller	3 mv rms PS 2014550	$1.08 \frac{deg/sec}{mv}$	3 deg/sec
Tachometer	15 mv rms PS 2016207	$3.70 \frac{deg/sec}{mv}$	55 deg/sec
SMT SHAFT			
Hand Controller	3 mv rms PS 2014550	$2.1 \frac{deg/sec}{mv}$	6 deg/sec
Tachometer	15 mv rms PS 2016207	$7.2 \frac{deg/sec}{mv}$	108 deg/sec

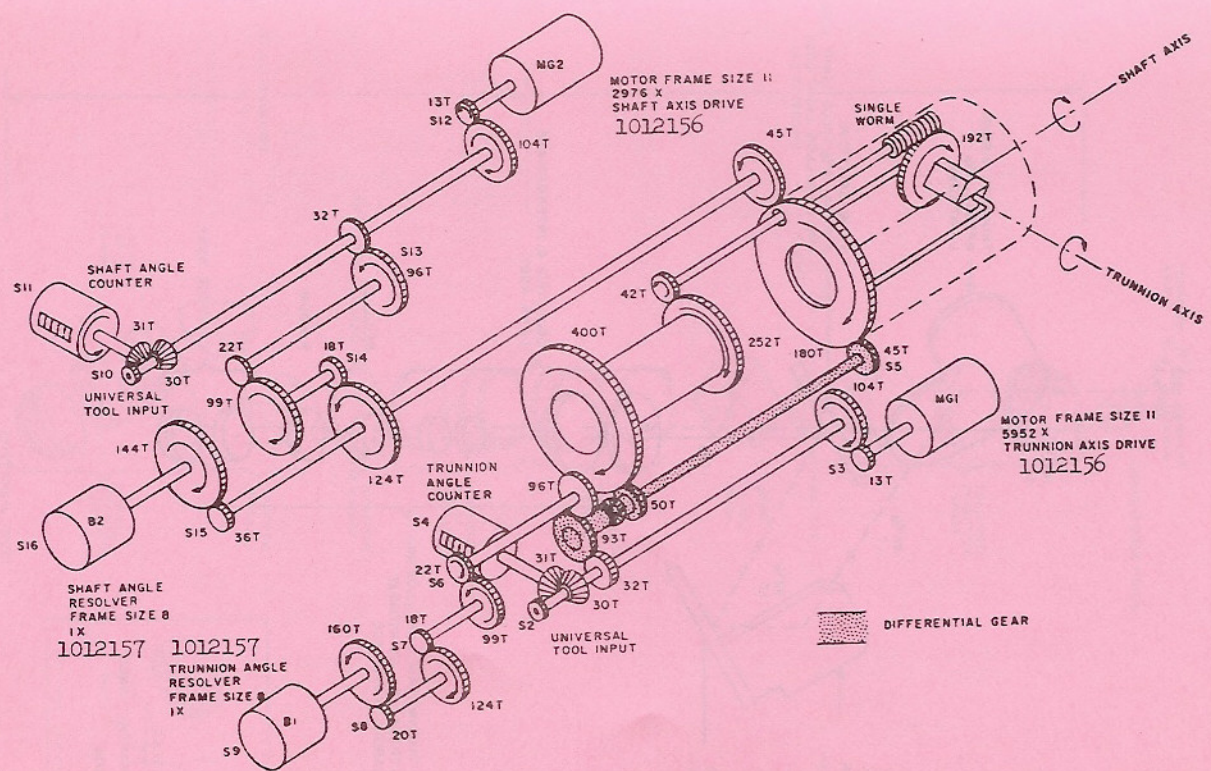
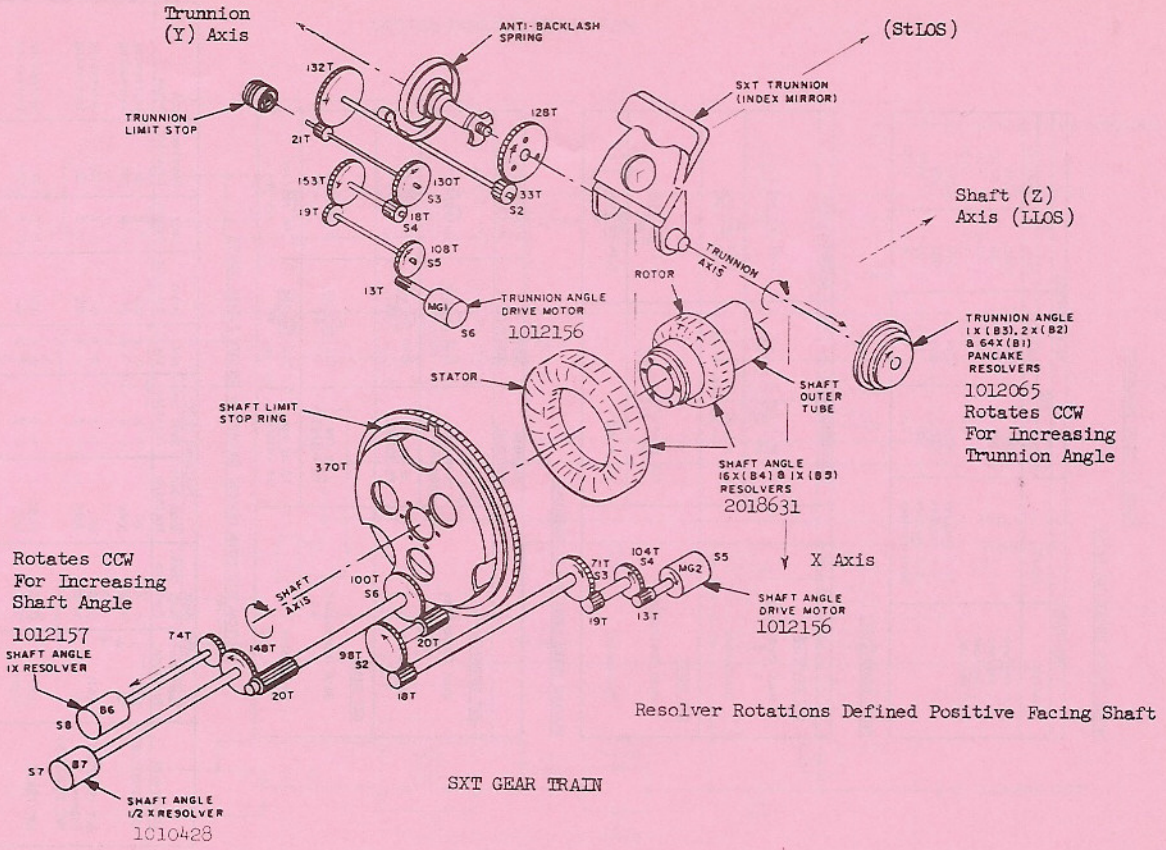
OPTICAL SUBSYSTEM PHASING (ROTATIONS DEFINED FACING SHAFT)

Integrator	Direct Mode Hand Cont. Position	Resolved Mode* Hand Cont. Position	MRA Input	Motor Rotation	Each Output	Resolver LOS
SXT TRUNNION	DOWN UP	UP DOWN	0 0	CCW CW	0 0	Decreasing Increasing
SMT TRUNNION (slaved to SXT TRUNNION)	DOWN UP	DOWN UP	0 0	CW CCW	0 0	Decreasing Increasing
SMT SHAFT	Left Right	Right Left	0 0	CCW CW	0 0	Decreasing Increasing
SMT SHAFT (slaved to SXT Shaft)	Left Right	Left Right	0 0	CW CCW	0 0	Decreasing Increasing

*1st quadrant only 0 < A_B < 90°

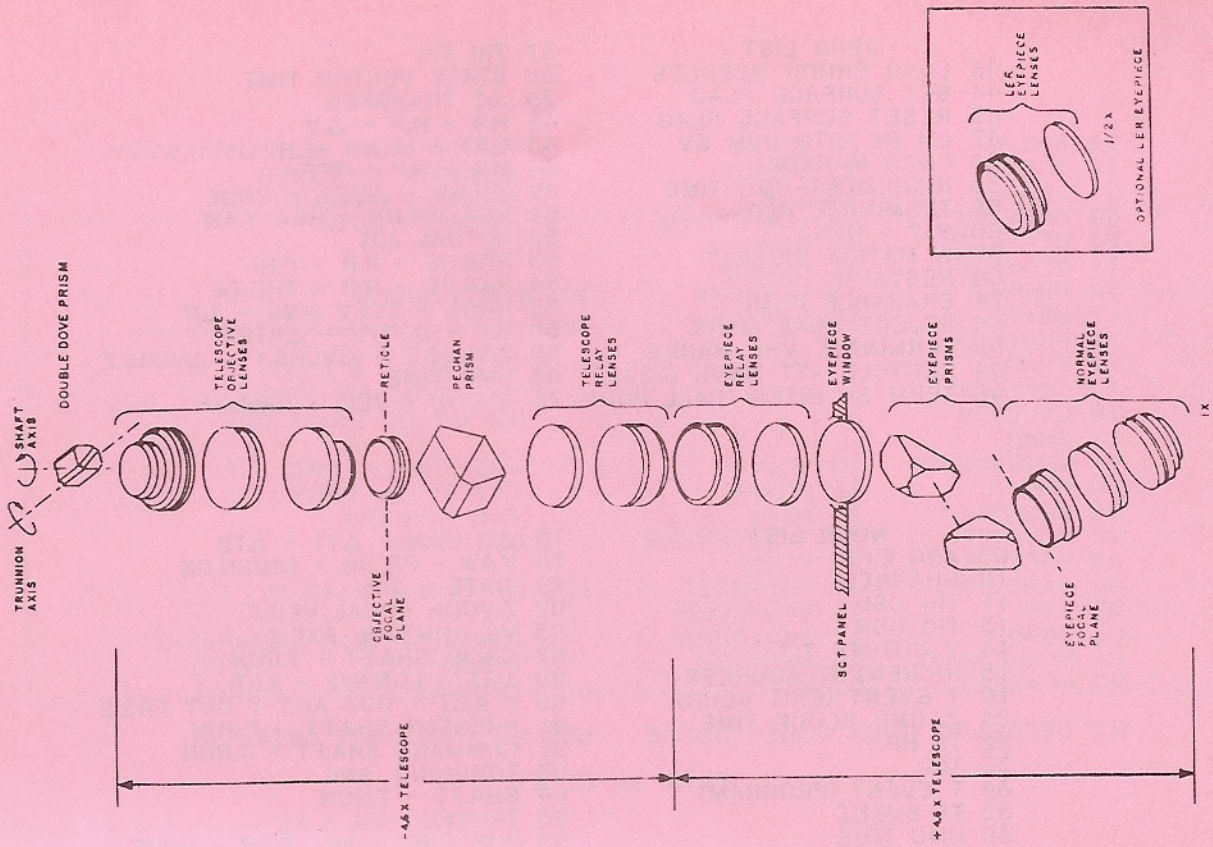
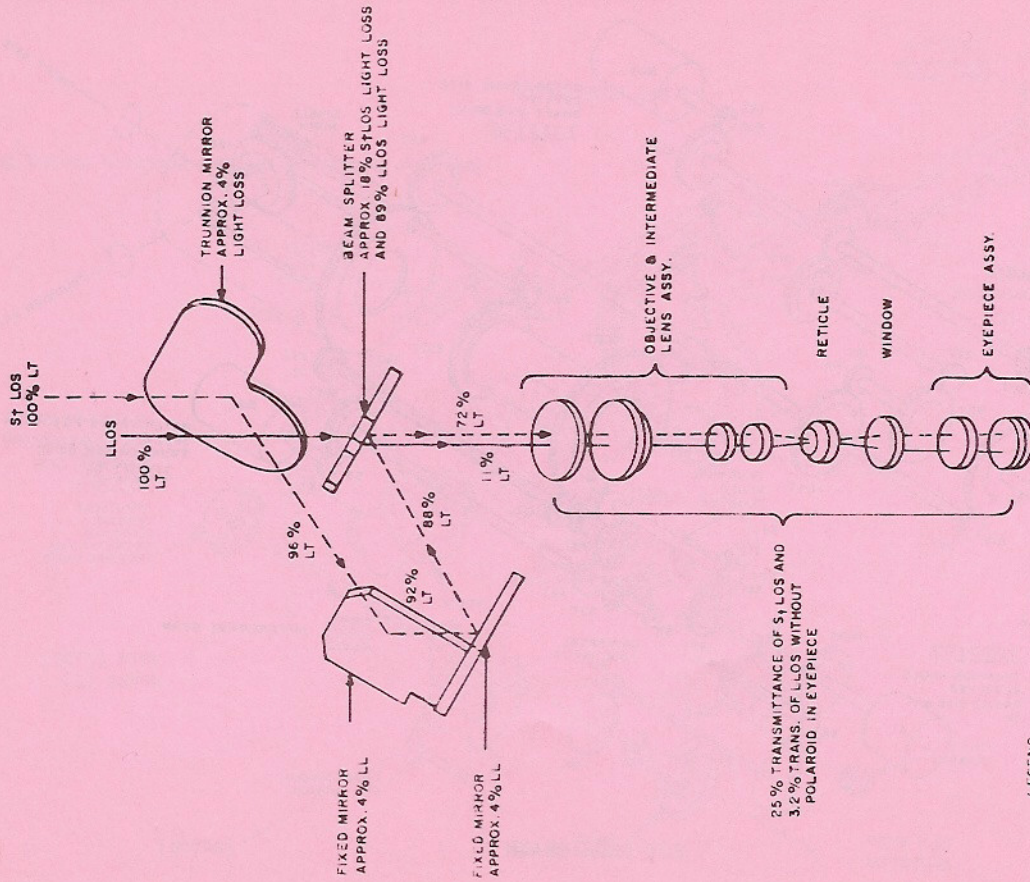
OPTICS TWO SPEED SWITCH





HW-64

HW-65



VERB LIST

43	LOAD ERROR NEEDLES	37	TIG TPI
44	SET SURFACE FLAG	38	STATE VECTOR TIME
45	RESET SURFACE FLAG	39	ΔT TRANSFER
47	LM SV INTO CSM SV	42	H.A - H.P - ΔV
54	COAS MARKING	43	LAT - LONG - .H
55	INCREMENT CMC TIME	44	H.A - H.P - TFF
56	TERMINATE P20	49	$\Delta P.OS$ - $\Delta V.E.L$ - CODE
60	N17 = N20	51	HGA PIT.CH - HGA Y.AW
67	W MATRIX DISPLAY	52	ACTUAL .WT
69	RESTART	53	RANGE - R.R - P.HI
74	ERASABLE DUMP	54	RANGE - R.R - THE.TA
86	REJECT COAS MARK	55	CODE - ELEV ANG - .WT
88	TERMINATE VHF MARKS	58	H.P - $\Delta VTP.I$ - $\Delta VTP.F$
94	RECYCLE ATT MNVR (P23)	59	$\Delta VLOS.X$ - $\Delta VLOS.Y$ - $\Delta VLOS.Z$
96	TERM SV INTEG (CALL P00)	65	CMC TIME
		73	H(X10) - VEL - GAM.MA

NOUN LIST

05	ANG E.RR	75	ΔH CD.H - $\Delta T1$ - $\Delta T2$
10	CHANNEL	78	Y.AW - PIT.CH - OMICRON
11	TIG CSI	79	.RATE - .DB
13	TIG CDH	82	$\Delta VCDH$ LOCAL VERT
14	V CUTOFF TLI	85	VG CONTROL AXES
15	INCREMENT ADDRESS	87	CALIB SHA.FT - T.RUN
16	T EVENT (EXIT VERB)	89	.LAT - LONG/2 - ALT
24	Δ CMC CLOCK TIME	90	Y ACT-Y DOT ACT-Y DOT PAS.S
32	TF HP	91	PRESENT SHA.FT - T.RUN
33	TIG	92	COMMAND SHA.FT - T.RUN
34	T EVENT (PROGRAM)	93	TORQUING .ANG
35	TF EVENT	94	SHA.FT - T.RUN
36	CMC TIME	95	TFI/TFC - VG - VI
		99	POS ERR. - VEL ER.R - CODE

NOUN 70 CODES

R1: CELESTIAL BODY CODE		OOODE	P22	R2: LANDMARK DATA	
00	PLANET	27	ALKAID	LANDING SITE	10001
01	ALPHERATZ	30	MENKENT	KNOWN SITE	10000
02	DIPHA	31	ARCTURUS	UNKNOWN SITE	20000
03	NAVI	32	ALPHECCA		
04	ACHERNAR	33	ANTARES	P23	R1: OOODE STAR ID
05	POLARIS	34	ATRIA		
06	ACAMAR	35	RASALHAGUE	R2: 00000	
07	MENKAR	36	VEGA	C=0 HORIZON	
10	MIRFAK	37	NUNKI	C=1 EARTH LDMK	
11	ALDEBARAN	40	ALTAIR	C=2 LUNAR LDMK	
12	RIGEL				
13	CAPELLA			R3: 00000 HORIZ ID	
14	CANOPUS			C=1 EARTH. 2 LUNAR	
15	SIRIUS			D=1 NEAR. 2 FAIR	
16	PROCYON				
17	REGOR	41	DABIH		
20	DNOCES	42	PEACOCK		
21	ALPHARD	43	DENEIB		
22	REGULUS	44	ENIF		
23	DENEbola	45	FOMALHAUT		
24	GIENAH	46	SUN		
25	ACRUX	47	EARTH		
26	SPIGA	50	MOON		

NOUN 99 CODES

R3:
00001 = RDZV
00002 = ORBITAL
00003 = CISLUNAR
00000 = NO REINITIALIZATION

ALARM CODES (V05N09)

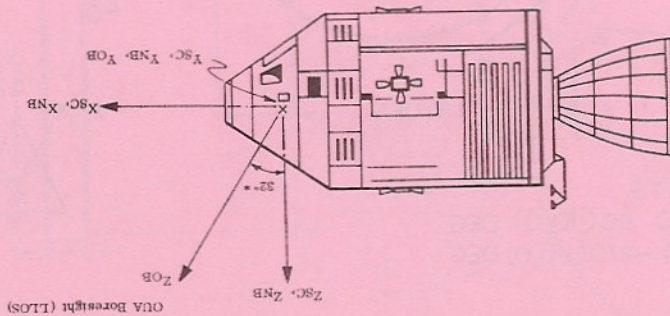
00602	HP < 5.8 NM AFTER CDH	00603	CSI TIG TO CDH TIG ΔT < 10 MIN
00603	CSI TIG TO CDH TIG ΔT < 10 MIN	00604	CDH TIG TO TPI TIG ΔT < 10 MIN
00605	TOO MANY ITERATIONS	00606	CSI ΔV > 1000 LAST 2 ITERATIONS
00611	NO TIG FOR ELEV ANGLE GIVEN	00612	WRONG SPHERE OR INFL AT TIG
00613	REENTRY ANGLE OUT OF LIMITS	00777	ISS WARNING-PIPA FAIL
00725	PIPA SATURATED	00777	ISS WARNING-IMU & PIPA FAIL
00726	CDU BAD DURING MK-REPEAT	07277	ISS WARNING-IMU & PIPA FAIL
00727	CDU SATURATED	04277	ISS WARNING-ICDU & PIPA FAIL
00728	CDU BAD DURING MK-REPEAT	03777	ISS WARNING-ICDU FAIL
00729	CDU SATURATED	01703	INSUF TIME TO INTEG-TIG SUP
00730	CDU SATURATED	01601	BAD IMU TORQUE
00731	CDU SATURATED	01600	OVERFLOW DURING DRIFT TEST
00732	CDU SATURATED	01520	V37 NOT PERMITTED NOW
00733	CDU SATURATED	01427	IMU POLARITY REVERSED
00734	CDU SATURATED	01426	IMU UNSATISFACTORY
00735	CDU SATURATED	01407	VG INCREASING
00736	CDU SATURATED	01301	ARCSIN/ARCCOS INPUT > ONE
00737	CDU SATURATED	01107	PHASE TABLE FAIL-ERASABLE BAD
00738	CDU SATURATED	01106	UPLINK TOO FAST
00739	CDU SATURATED	01105	DOWNLINK TOO FAST
00740	CDU SATURATED	01102	CMC SELF TEST ERROR
00741	CDU SATURATED	01102	ISS WARNING-PIPA FAIL
00742	CDU SATURATED	00613	REENTRY ANGLE OUT OF LIMITS
00743	CDU SATURATED	00612	WRONG SPHERE OR INFL AT TIG
00744	CDU SATURATED	00611	NO TIG FOR ELEV ANGLE GIVEN
00745	CDU SATURATED	00606	CSI ΔV > 1000 LAST 2 ITERATIONS
00746	CDU SATURATED	00605	TOO MANY ITERATIONS
00747	CDU SATURATED	00604	CDH TIG TO TPI TIG ΔT < 10 MIN
00748	CDU SATURATED	00603	CSI TIG TO CDH TIG ΔT < 10 MIN
00749	CDU SATURATED	00602	HP < 5.8 NM AFTER CDH
00750	CDU SATURATED	00600	NO SOLUTION ON 1ST ITERATION
00751	CDU SATURATED	00421	W MATRIX OVERFLOW
00752	CDU SATURATED	00406	P20 NOT OPERATING
00753	CDU SATURATED	00405	STAR PAIR NOT AVAILABLE
00754	CDU SATURATED	00404	TGT OUT OF VIEW (TRUN > 90 DEG)
00755	CDU SATURATED	00402	DO SECOND MINKEY PULSE TORQUE
00756	CDU SATURATED	00401	DESIRED MGA TOO LARGE
00757	CDU SATURATED	00220	IMU NOT ALIGNED/TORQUING PROBLEM
00758	CDU SATURATED	00217	COARSE ALIGN/TORQUING PROBLEM
00759	CDU SATURATED	00214	PROGRAM USING IMU BUT IMU OFF
00760	CDU SATURATED	00213	IMU NOT ON-TURN ON IMU
00761	CDU SATURATED	00212	PIPA FAIL BUT PIPA NOT IN USE
00762	CDU SATURATED	00211	COARSE ALIGN ERROR > 2 DEG
00763	CDU SATURATED	00210	IMU NOT OPERATING
00764	CDU SATURATED	00207	NEED 90 SEC FOR ISS TURN ON
00765	CDU SATURATED	00206	CANNOT ZERO ICDU-WITH ALARMS
00766	CDU SATURATED	00205	PIPA SATURATED
00767	CDU SATURATED	00202	ISS WARNING-IMU & PIPA FAIL
00768	CDU SATURATED	00201	ISS WARNING-IMU & PIPA FAIL
00769	CDU SATURATED	00120	ZERO OPTICS FIRST
00770	CDU SATURATED	00117	VAIN91 BUT OPTICS NOT READY
00771	CDU SATURATED	00116	OPTICS SW MOVED BEFORE 15 SEC
00772	CDU SATURATED	00115	VAIN91 WITH OPTICS NOT IN CMC
00773	CDU SATURATED	00114	MORE MKS THAN DESIRED
00774	CDU SATURATED	00113	NO INBITS (CH 16)
00775	CDU SATURATED	00112	CDU BAD DURING MK-REPEAT
00776	CDU SATURATED	00601	HP < 5.8 NM AFTER CSI

NOTE :

00404 * IS A PRIORITY ALARM
 2XXXX FOODOC-NO RESTART LT-F37
 (AVE G ON OR EXT VERB-
 ACTS AS BAILOUT)
 3XXXX BAILOUT-NO RESTART LT-
 PROGRAM CONTINUES

14777 ISS WARN-IMU,ICDU & PIPA FAIL
 20430 ORBITAL INTEGRATION STOPPED
 20607 NO CONIC SOLUTION
 20610 ALTITUDE < 400K AT TIG (P37)
 21204 NEG OR ZERO WAITLIST CALL
 21206 TWO DISPLAYS ATTEMPTED AT ONCE
 21210 SORT CALLED WITH NEG ARGUMENT
 21302 DSKY ALARM DURING INTERNAL USE
 21501 ILLEGAL FLASHING DISPLAY
 21502 KEYED POI AFTER LIFTOFF
 31104 DELAY ROUTINE IS BUSY
 31201 EXEC OVERFLOW-NO VAC AREAS
 31202 EXEC OVERFLOW-NO CORE SETS
 31203 WAITLIST OVERFLOW-MANY TASKS
 31211 CANNOT INTERRUPT EXTENDED VERB

OUA LINE OF SIGHT TO IMU STABLE MEMBER TRANSFORMATIONS



$$\begin{bmatrix} X \\ Y \\ Z \end{bmatrix}^{SM} = \begin{bmatrix} \cos AI & 0 & \sin AI \\ \sin AM & \cos AM & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} \cos AO & \sin AO \\ \cos AO & -\sin AO \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \cos AS & \sin AS \\ \cos AS & -\sin AS \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \cos AT & \sin AT \\ \cos AT & -\sin AT \\ 0 & 0 \end{bmatrix} \begin{bmatrix} X_{NB} \\ Y_{NB} \\ Z_{NB} \end{bmatrix}$$

* Nominal Angle 32° 31' 23"

$$\begin{bmatrix} X \\ Y \\ Z \end{bmatrix}^{SM} = \begin{bmatrix} \cos AI & 0 & \sin AI \\ \sin AM & \cos AM & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} \cos AO & \sin AO \\ \cos AO & -\sin AO \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \cos AS & \sin AS \\ \cos AS & -\sin AS \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \cos AT & \sin AT \\ \cos AT & -\sin AT \\ 0 & 0 \end{bmatrix} \begin{bmatrix} X_{NB} \\ Y_{NB} \\ Z_{NB} \end{bmatrix}$$

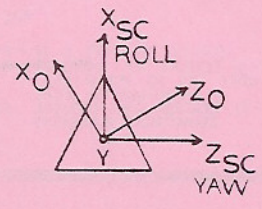
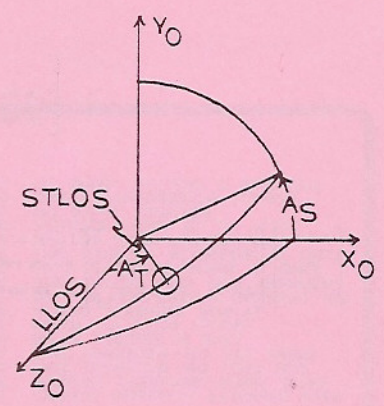
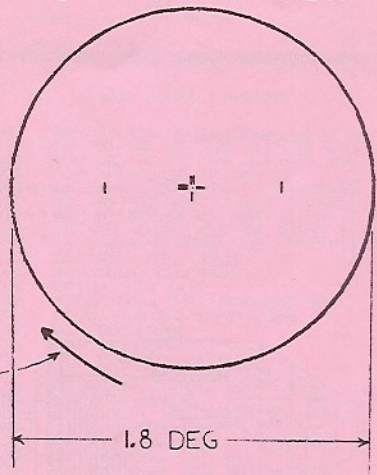
where

AI, AM and AO are the inner, middle, and outer gimbal angles as indicated by the CMC AS and AT are the SXT LOS shaft and trunnion angles

SXT FIELD OF VIEW

DRIVE RATES

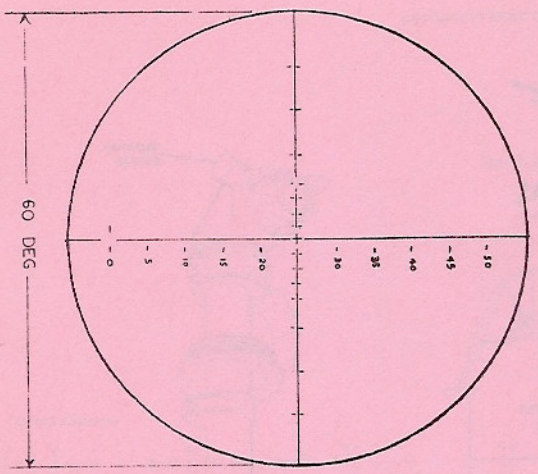
	TRUNNION	SHAFT
HI	10.0 DEG/S	19.5 DEG/S
MED	1.0 DEG/S	2.0 DEG/S
LO	0.1 DEG/S	0.2 DEG/S
MIN	25 SEC/S	50 SEC/S
CMC	3.8 DEG/S	15.1 DEG/S



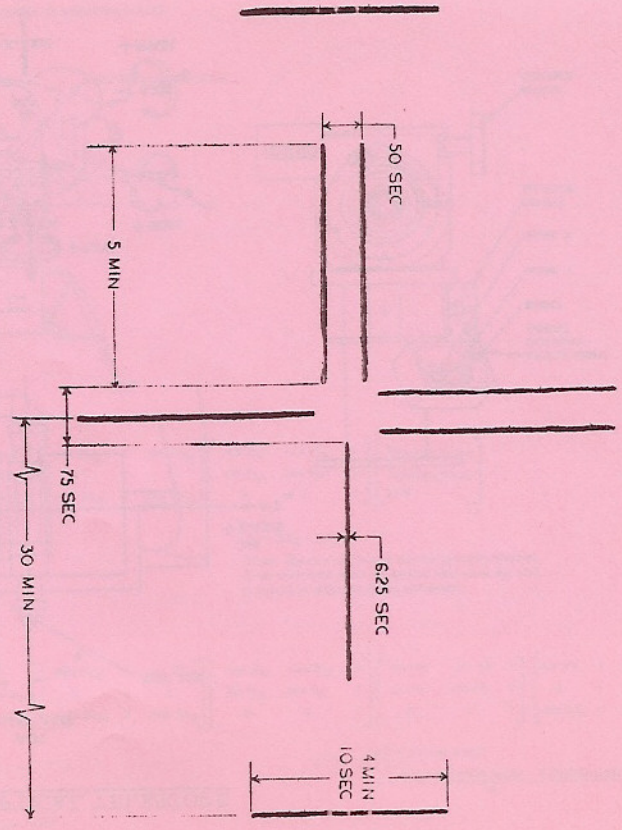
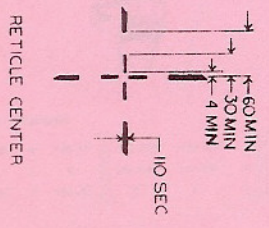
RETICLE ROTATION WITH OPTICS HAND CONTROLLER FULL RIGHT (MANUAL DIRECT MODE)

SXT LOS MECHANICAL LIMITS

TRUNNION 0(0,-10) TO 90(10,0) DEG
 SHAFT 270(0,-10) TO -270(-10,0) DEG

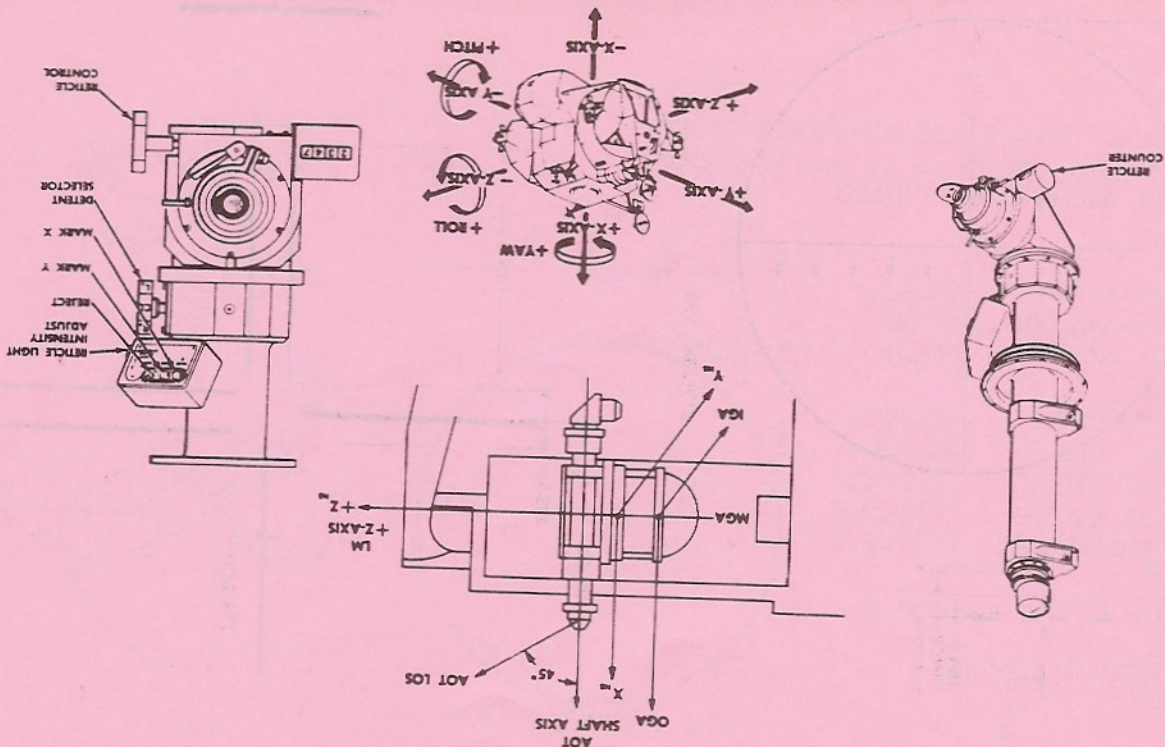


SCT RETICLE

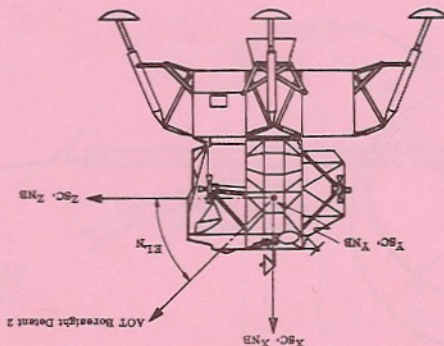


SXT VACUUM RETICLE

ALIGNMENT OPTICAL TELESCOPE



HW-74



AOT LINE OF SIGHT TO IMV STABLE MEMBER TRANSFORMATIONS

Optical Alignment

$$\begin{bmatrix} X \\ Y \\ Z \end{bmatrix} = \begin{bmatrix} \cos A_I & 0 & \sin A_I \\ \sin A_M & \cos A_M & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} \cos A_I \\ \sin A_M \\ 1 \end{bmatrix}$$

$$\begin{bmatrix} X \\ Y \\ Z \end{bmatrix} = \begin{bmatrix} \cos A_I & 0 & \sin A_I \\ \sin A_M & \cos A_M & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} \cos A_I \\ \sin A_M \\ 1 \end{bmatrix}$$

$$\begin{bmatrix} X \\ Y \\ Z \end{bmatrix} = \begin{bmatrix} \cos A_I & 0 & \sin A_I \\ \sin A_M & \cos A_M & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} \cos A_I \\ \sin A_M \\ 1 \end{bmatrix}$$

N = Deflect Position

$$R_N = AZ_2 - AZ_N$$

(Note: R_N is a correction for the apparent rotation of the star field about the optical axis when the AOT is moved to different deflect positions.)

Turner Bore Sight Alignment:

$$\begin{bmatrix} X \\ Y \\ Z \end{bmatrix} = \begin{bmatrix} \cos A_I & 0 & \sin A_I \\ \sin A_M & \cos A_M & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} \cos A_I \\ \sin A_M \\ 1 \end{bmatrix}$$

$$\begin{bmatrix} X \\ Y \\ Z \end{bmatrix} = \begin{bmatrix} \cos A_I & 0 & \sin A_I \\ \sin A_M & \cos A_M & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} \cos A_I \\ \sin A_M \\ 1 \end{bmatrix}$$

where

$A_I, A_M,$ and A_O are the inner, middle, and outer gimbal angles as indicated by the LOS

AZ_N and EL_N are the AOT azimuth and elevation angles at the Nth deflect.

$EL_N \sim 45^\circ, N=1, 2, \dots, 6; AZ_1 \sim 60^\circ; AZ_2 \sim 90^\circ; AZ_3 \sim 120^\circ; AZ_4 \sim 150^\circ; AZ_5 \sim 180^\circ; AZ_6 \sim 120^\circ$

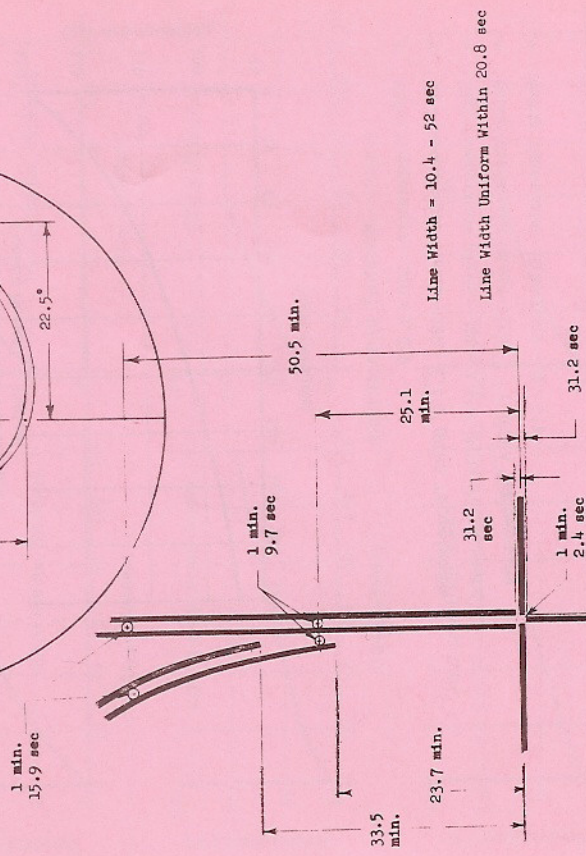
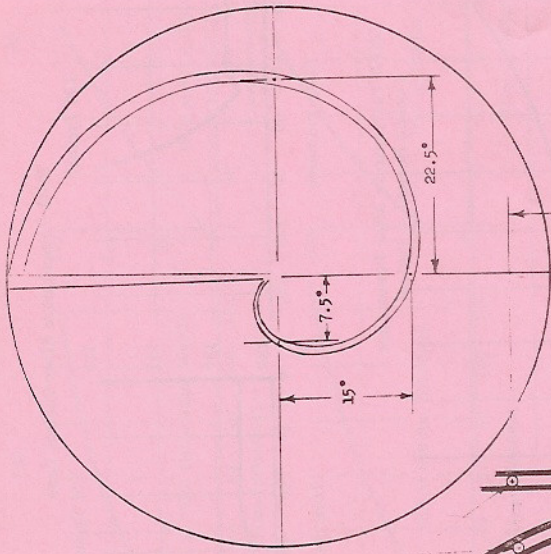
$$AT = 360 - \theta \text{ Reticle Angle} - Y \text{ Reticle Angle}$$

12

AS = Y Reticle Angle

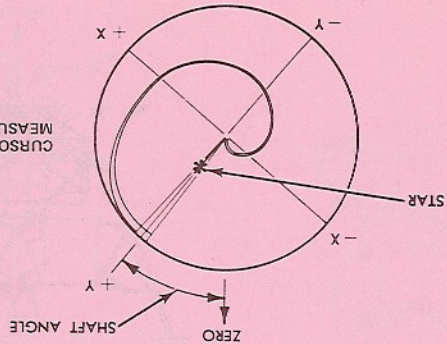
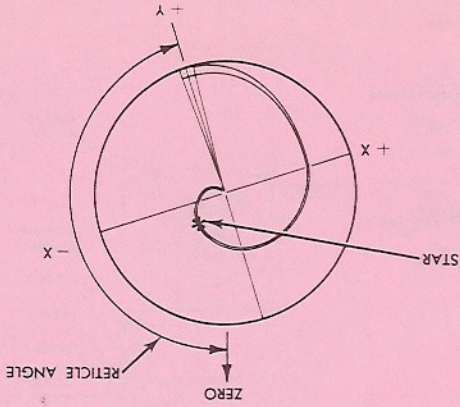
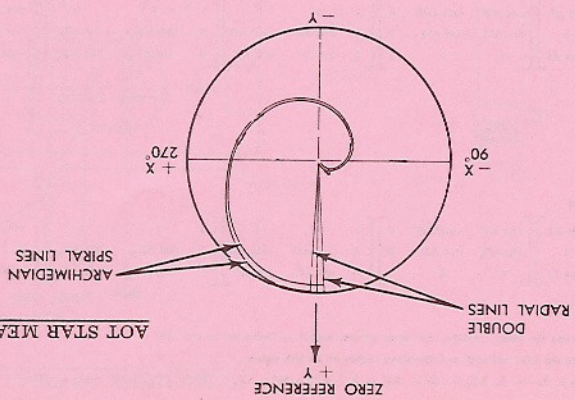
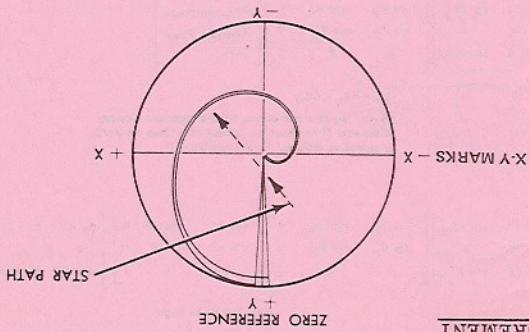
$$\begin{bmatrix} X \\ Y \\ Z \end{bmatrix} = \begin{bmatrix} \cos A_I & 0 & \sin A_I \\ \sin A_M & \cos A_M & 0 \\ 0 & 0 & 1 \end{bmatrix} \begin{bmatrix} 0 \\ 0 \\ 1 \end{bmatrix} = \begin{bmatrix} \cos A_I \\ \sin A_M \\ 1 \end{bmatrix}$$

HW-75



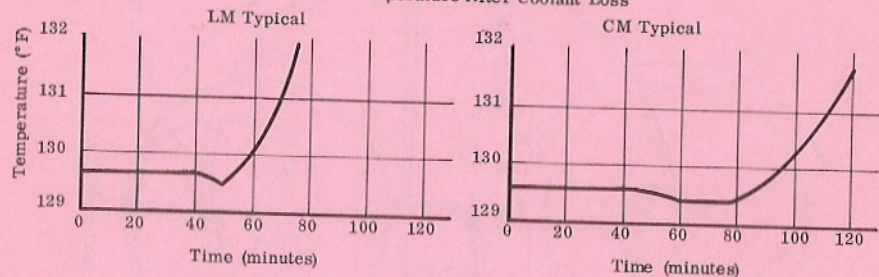
AOT RETICLE

RETICLE CENTER

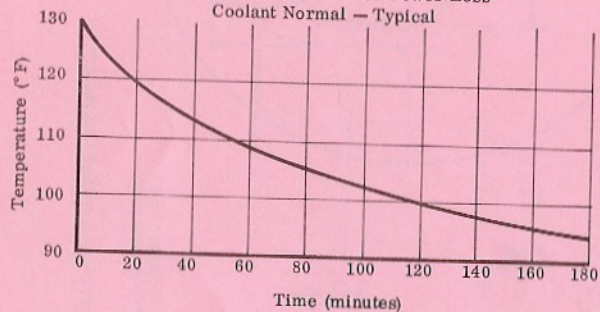


TYPICAL IMU TEMPERATURE

PIPA Temperature After Coolant Loss



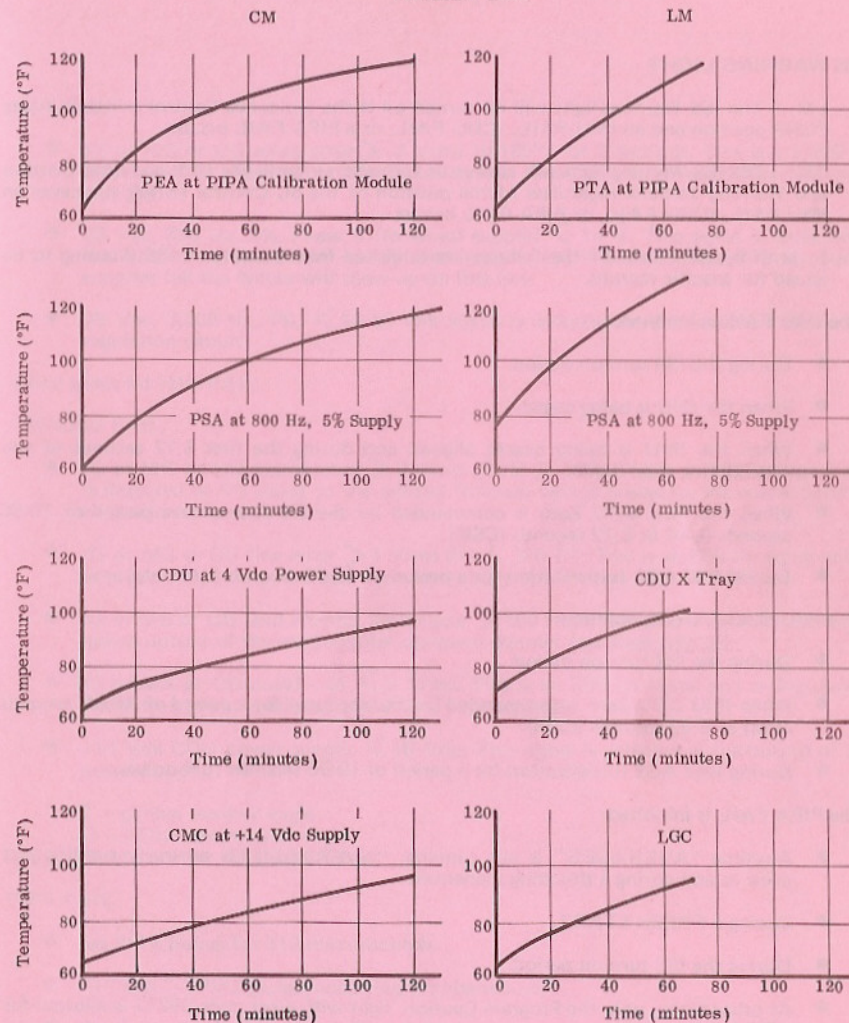
PIPA Temperature After Power Loss
Coolant Normal - Typical



NOMINAL PARAMETERS

- PIPA Temperature - $130 \pm 1, 5^\circ\text{F}$
- Low Temperature Alarm - $126 \pm 2, 5^\circ\text{F}$ (PIPA Temperature)
- High Temperature Alarm - $135 \pm 2, 5^\circ\text{F}$ (PIPA Temperature)
- Safety Thermostat Opens - $140 \begin{matrix} +5 \\ -2 \end{matrix}^\circ\text{F}$ (PIPA Temperature)
- Safety Thermostat Hysteresis - $2 - 5^\circ\text{F}$

TYPICAL HEADER TEMPERATURES AFTER COOLANT LOSS



ISS WARNING LIGHT

LM — The ISS Warning light will be turned on if the Guidance Control switch is in the PGNS position and an IMU FAIL, ICDU FAIL, or a PIPA FAIL occurs.

CM — The ISS Warning light will always be turned on in both the LEB and CMP Caution and Warning stations regardless of the position of the SC Control switch whenever an IMU FAIL, ICDU FAIL, or PIPA FAIL occurs.

In both systems each of the failures are inhibited from causing an ISS Warning to be issued for specific reasons.

The IMU FAIL is inhibited:

- During the ISS turn-on period.
- When the IMU is being caged.
- When the IMU is being coarse aligned and during the first 5.12 seconds of the ensuing fine align mode.
- When the IMU CDU Zero is commanded by the astronaut for a period of 10.56 seconds (LM) or 8.22 seconds (CSM).
- During R47 AGS Initialization for a period of 10.56 seconds (LM only).

The IMU CDU FAIL is inhibited:

- During the ISS turn-on period.
- When IMU CDU Zero is commanded by the astronaut for a period of 10.56 seconds (LM) or 8.22 seconds (CSM).
- During R47 AGS Initialization for a period of 10.56 seconds (LM only).

The PIPA FAIL is inhibited:

- Anytime "AVERAGEG" is not running. "AVERAGEG" is normally running just prior to and during a thrusting maneuver.
- During a FRESH START.
- During the ISS turn-on period.
- At other times, only the Program Caution light with error code 00212 is allowed for a PIPA FAIL.

The Program Caution light on the DSKY will always be turned on when the computer issues an ISS Warning.

IMU FAIL

- IG or MG or OG servo error $> 3 \text{ Vrms}$ (0.166°) for 2 seconds. This is a 3,200 Hz signal and is detected at the input to the respective servo amplifiers (see page HW-23).
- 28 Vac, 800 Hz, 5%, Phase B (to wheel supply) $< 14 \text{ V}$. This signal is detected at the output of the power supply and is such that if any 28 Vac, 800 Hz power supplies fail the failure will show up on this line.
- 28 Vac, 3,200 Hz, 1%, $< 14 \text{ V}$. This signal is detected on the feedback line in the regulation circuit.

Error codes for IMU FAIL.

IMU CDU FAIL

- IG or MG or OG coarse error $> 2 \text{ Vrms}$ ($\theta - \psi > 30^\circ$). This is an 800 Hz signal and is detected at the input to the emitter follower circuit preceding the coarse Schmitt trigger (see page HW-37).
- IG or MG or OG fine error $> 1 \text{ Vrms}$ ($\theta - \psi > 0.7^\circ$). This is an 800 Hz signal and is detected at the output of the main summing amplifier (see page HW-40).
- IG or MG or OG read counter limit cycle $> 160 \text{ Hz}$. This signal is detected from the upline output of the read counter up-down counter (see page HW-35).
- IG or MG or OG $\cos(\theta - \psi) < 2 \text{ Vrms}$. This is an 800 Hz signal and is detected at the output of the ladder amplifier (see page HW-40).
- +14 Vdc CDU power supply $< +8 \text{ Vdc}$. This signal is detected at the output of the power supply.

θ — gimbal resolver angle
 ψ — CDU read counter angle
 Error code for IMU CDU FAIL

PIPA FAIL

- No PIPA pulses for 312 microseconds.
- "+" and "-" PIPA pulses occurring simultaneously.
- No "+" and "-" pulses for 1.28 to 3.84 seconds.

OPTICS CDU FAIL

Same as IMU CDU FAIL with the exception that there is no coarse error signal.

RR CDU FAIL

Same as IMU CDU FAIL.

AGC/IDC Warning

The AGC/IDC Warning light will be turned on as a result of the following:

- Scalar Failure
- Scalar Double Failure
- AGC/IDC Power Failure
- AGC/IDC Counter Failure
- AGC/IDC V FAIL (STANDBY)
- AGC/IDC Restart (Hardware)

In addition, the Pulse Torque Power Supply Inhibit is generated every time a AGC/IDC Warning is generated. The effect is to open up the accelerometer loops and prevent any pulse torquing of EVRS.

Scalar Failure

Occurs if Stage 17 of the scalar fails to produce pulses. This is the 1.28 second period scalar stage and thus a check of timing for all alarms. This failure causes immediate turn on of the AGC/IDC Warning Light.

Scalar Double Failure

Occurs if the 100 pulse per second stage (.010 second period) operates at 200 pps. This provides a check on the scalar stability.

AGC/IDC Power Failure

Occurs when the prime +28 VDC power to the computer is lost. This failure causes an immediate turn on of the AGC/IDC Warning Light.

Counter Failure

Occurs if counter increments happen too frequently or else fail to happen following an increment/decrement request. "Too frequently" means continuous counter requests and/or incrementing from .655 to 1.875 milliseconds.

Examples of counters are:

- AGC/IDC input/output, (Time 1 and Time 2), ± CUI X, Y, Z, PIPA X, Y, Z,
- shaft and trunnion angles, Rate Hand Controllers R, P, Q, etc.

AGC/IDC Voltage FAIL (STANDBY)

The occurrence of a Voltage Failure while the computer is in the STANDBY mode will cause the AGC/IDC Warning Light to be turned on. The causes of a Voltage Failure are given below in the RESTART section.

RESTARTS

A RESTART (hardware) and subsequent AGC/IDC Warning is generated for the following alarms:

- Oscillator Failure
- Transfer Control (TC) Trip
- Parity Alarm
- Nightwatchman Fail
- Interrupt (RUPF) Lock
- Voltage Fail

The RESTART inhibits access to memory temporarily, freezes the computer, stores in process information and then transfers control to address 4000. This address has the information address for the next instruction after a RESTART that the software programmer has provided.

In general, the programmer has chosen some particular point in a program to resume operation after a RESTART and operation continues with only small inconsequential losses of information of time and/or thrust.

Oscillator Fail

Occurs if loss of oscillator's 1.024 milliseconds square wave happens. In addition a logic circuit insures a RESTART condition for a 250 millisecond interval upon transferring from STANDBY to OPERATE.

Transfer Control (TC) Trip

Occurs if too many or too few TC instructions are requested. The period for "too many" or "too few" is from 5 to 15 milliseconds in duration.

Parity Alarm

Occurs if any accessed word in fixed or erasable memory whose address is 10, or greater contains an even number of "ones." All locations of 10, or greater are stored in fixed or erasable memory with odd parity.

Nightwatchman Fail

Occurs if the computer should fail to access address 67 within a period whose duration varies from 0.64 to 1.92 seconds.

Nightwatchman assures that the computer is still operating during an extended idle period and is not tied up in some interrupt loop.

Interrupt (RUPF) Lock

Occurs if an interrupt is either "too long" or "too infrequent." The time period for "too long" or "too infrequent" varies from 140 milliseconds to 300 milliseconds.

Voltage Fail

Occurs if the AGC/IDC voltages (+28, +14, or +4 VDC) are out of limits for 157 to 470 microseconds.

These limits are:

- +28 VDC supply < 22.6 VDC or 20.3* VDC ±0.2 VDC
- +16 VDC < +14 VDC supply > +12.5 VDC
- +4, +4 VDC < 4 VDC supply > +3.65 VDC

*No types of modules are in existence. Each computer has its own particular module.

When the computer is in the OPERATE mode a RESTART is generated for Voltage Failure. If the Computer is in the STANDBY mode, the Voltage Fail is processed through the warning filter to turn on the AGC/IDC Warning Light.

All of the signals with the exception of Scalar failure or a prime power (+28 VDC) failure are processed through a buffer filter that prevents momentary transients from generating failures.

The filter operates such that an output will occur if input pulses occur at a rate of greater than 0.9 pps or 6 consecutive stretched (160 milliseconds) pulses occur or a single event longer than .960 seconds, then the AGC/IDC warning lasts for a minimum of 5 seconds.

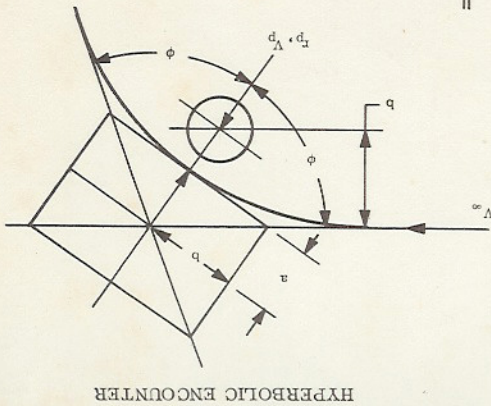
V35E - Test Lights

The computer has a test light routine, called by V35E, that tests all the lights on the DESKY. All "0's" and "+" signs are displayed along with each light turned on for 5 seconds minimum.

After the 5 seconds are completed, the lights are turned off. Note that this verb also turns off the Pulse Torque Power Supply which then causes the PIPA loops to open for approximately 5 seconds. This should be avoided if possible. The routine can be called when ISS operate is not on.

REFERENCES

1. Guidance System Operations Plan for Manned LM Earth Orbital and Lunar Missions Using Program Luminary1C, MIT Charles Stark Draper Laboratory, Report No. R-567, Section 5, November 1969.
2. Spacecraft Operational Trajectory for Apollo 16 (Mission J-2), Launched April 16, 1972, Volume I, Mission Profile, MSC Internal Note No. 72-FM-39, 14 March 1972.
3. Spacecraft Operational Trajectory for Apollo 16 (Mission J-2), Launched April 16, 1972, Volume II, Trajectory Parameters, MSC Internal Note No. 72-FM-39, 10 February 1972.
4. Final Apollo 16 Flight Plan, NASA, March 6, 1972.
5. Guidance System Operations Plan for Manned CM Earth Orbital and Lunar Missions Using Program Colossus 3, MIT Charles Stark Draper Laboratory, Report No. R-577, Section 4, Rev. 16, April 1971.
6. Guidance System Operations Plan for Manned LM Earth Orbital and Lunar Missions Using Program Luminary 1C, MIT Charles Stark Draper Laboratory, Report No. R-567, Section 4, December 1969.
7. Guidance System Operations Plan for Manned CM Earth Orbital and Lunar Missions Using Program Colossus 3, MIT Charles Stark Draper Laboratory, Report No. R-577, Section 5, Rev. 14, March 1971.
8. Guidance, Flight Mechanics and Trajectory Optimization, Volume XIV - Entry Guidance Equations, NASA CR-1013, April 1968.
9. E Guidance - A General Explicit, Optimizing Guidance Law for Rocket - Propelled Spacecraft, MIT Charles Stark Draper Laboratory, Report No. R-456, August 1964.
10. Guidance, Navigation, and Control, Lunar Module Functional Description and Operation Using Flight Program Luminary, MIT Charles Stark Draper Laboratory, Report No. E-2260, March 1969.
11. Apollo Operations Handbook, Lunar Module LM5 and Subsequent, Volume I, Subsystems Data, LMA790-3-LM, Grumman Aircraft Engineering Corporation, December 15, 1968.
12. Universal Lunar Module Systems Handbook, LM 4 and Subsequent Vehicles, FCO27, NASA, 17 January 1969.
13. Apollo Operations Handbook, Lunar Module LM5 and Subsequent, Volume II, Operational Procedures, LMA 790-3-LM, Grumman Aircraft Engineering Corporation, 1 May 1969.
14. Radar Section Study Guide Lunar Module LM-4, LSG 770-154-5-LM-4, Grumman Aircraft Engineering Corporation, January 1969.
15. Control Electronics Section Study Guide Lunar Module LM-4, LSG 770-154-7-LM-4, Grumman Aircraft Engineering Corporation, January 1969.
16. Notes on the Block II ECDU, AP-M-#17329, October 23, 1967.
17. Block II 25 IRIG Pulse Torquing Circuit, XDE 34-R-20, June 30, 1967.
18. Block II Stab-Amp, AP-M-#15614, April 12, 1967.
19. Notes on G&N JDC's 12220 and 12619 - The Gimbal Response Test, AP-M-#18834, May 1, 1968.
20. ECDU Computer Operate Optics, AP-M-#18281, February 26, 1968.
21. Orbital Navigation Via Landmark Tracking, AP-M-#21695, February 24, 1969.
22. Celestial Navigation, AP-M-#21569, February 4, 1969.
23. Notes on the Block II Coarse Align Loop, AP-M-#16949, Rev. 1, November 17, 1967.
24. Block II Optical Subsystem Test Results, XDE 34-T-57, Rev. D, September 6, 1969.
25. Notes on the Block II and LEM PPA, AP-M-#17714, December 11, 1967.
26. AOT Usage During LM Operations on the Apollo 9 Mission, AP-M-#21623, February 6, 1969.
27. Astronautical Guidance, Richard H. Battin, McGraw-Hill Book Company, 1964.
28. Apollo 16 LM Timeline Book, NASA
29. Users' Guide to Apollo G&NCS Major Modes and Routines, Rev. 3, Colossus 3 and Luminary 1E, MIT Charles Stark Draper Laboratory Report No. E-2448, May 1971.
30. Comments on Delco Apollo 15 G&N Summary, TRW A-201, 4900.5-222, July 28, 1971.



HYPERBOLIC ENCOUNTER

$$V_{\infty}^2 = V_d^2 - 2\mu/r_d = \mu/a$$

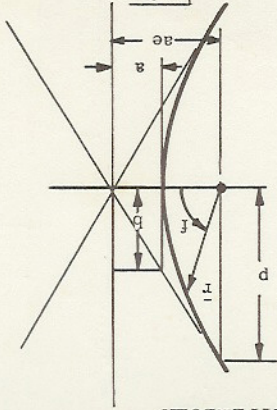
$$V_d V_{\infty} = b V_{\infty}^2$$

$$\tan \phi = b/a = b V_{\infty}^2 / \mu$$

$$\mu = GM_M$$

$$t^2/s^2$$

$$M_M = \text{Mass of the Moon}$$



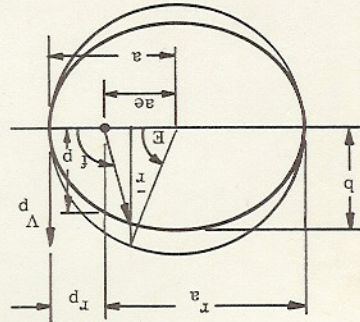
HYPERBOLA

$$\frac{V_{\infty}^2}{\mu} = \frac{1}{a}$$

$$a(e^2 - 1) = \mu \left[\frac{1}{2} \left(\frac{r}{a} + 1 \right) \right]$$

$$\frac{1 + e \cos \theta}{a(e^2 - 1)} = \frac{1}{r}$$

$$\sqrt{1 + \left(\frac{b}{a}\right)^2} > 1$$



ELLIPSE

$$b = \sqrt{a^2 - c^2}$$

$$a = \frac{r_a + r_p}{2}$$

$$e = \frac{c}{a} = \sqrt{1 - \left(\frac{b}{a}\right)^2} < 1$$

$$r = \frac{a(1 - e^2)}{1 + e \cos \theta} = a(1 - e \cos \theta)$$

$$V = \left[\frac{\mu}{a} \left(\frac{1 - e^2}{1 + 2e \cos \theta + e^2} \right) \right]^{1/2}$$

$$p = a(1 - e^2)$$

Semi-minor Axis
Semi-major Axis
Eccentricity
Position
Velocity
Semi-latus Rectum