Practice:

Modeling is utilized for the analysis of conducted and radiated electromagnetic interference (EMI) caused by an electrostatic discharge (ESD) event. The modeling requires the combined use of a SPICE1 (Ref. 1), or other circuit analysis code and a wire antenna code based on the method of moments, and is primarily applicable to wires, cables, and connectors.

Benefit:

The use of a combined SPICE circuit analysis code and a method of moments code for the study of possible conducted and radiated EMI resulting from an ESD event, allow the assessment of EMI noise coupling onto electronic circuit interfaces (Ref. 2).

Programs That Certified Usage:

TOPEX (Topographic Oceanographic Experiment)

Center To Contact For More Information:

Jet Propulsion Laboratory (JPL)

Implementation Method:

Background:

Surface charging can develop on satellite exterior surfaces as a result of the interactions between the surfaces and the space plasma environment. Considerable research has been done in assessing charging mechanisms that are present when the dielectric surfaces of a spacecraft are exposed to the space plasma environment (at GEO and POLAR orbit primarily). Furthermore, software tools have been developed and utilized for modeling the charging potentials that might be present at several spacecraft locations for a given charging plasma environment. These analysis tools have been useful in the design and configuration of thermal blankets, and other dielectric surfaces on spacecraft.

No conclusive theory has yet been developed, but some work has been done in the analysis of the discharge process itself, which occurs when the released charge goes from one surface to another. Nevertheless it is widely accepted that an ESD can occur under vacuum (pressure <10^5 torr) when either: (a) dielectric surface voltage is greater than 500 V or (b) the electric field between a dielectric surface and a conductor is greater than 10^5 volts/cm.

Finally, when an electrostatic discharge occurs, the release of stored charge creates a discharge current that can generate conducted emissions and radiated emissions. Conducted emissions occur as a result of the replacement current that originates

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1 Simulator Program with Integrated Circuit Emphasis (SPICE)
when charge is blown off the dielectric surface inducing a replacement current to flow from the satellite structure. Radiated emissions are generated by the ESD current pulse.

The rapid surface potential change induces noise in circuits through capacitive coupling. The discharge current can also induce an inductively coupled signal into the victim circuit. Furthermore, radiated emissions can cause diverse forms of field-to-circuit coupling. Capacitive, inductive and field-to-circuit coupling scenarios are manifestations of conducted and radiated EMI, and are characterized by electrical noise on a victim electronic component or circuit. This induced electrical noise, which is transient by nature, can interfere with nominal current and voltage levels of electronic components causing transient data or command interruptions or, even worse, permanent component damage.

Analysis Steps:

I. ESD-EMI Topology: This is the first step in EMI analysis from ESD events. A study is performed of spacecraft assemblies and sub-assemblies to identify: a) the most likely locations for spacecraft charging and ESD to occur, b) the most likely location and types of conductive path(s) which will be followed by the induced current after an ESD event, and c) the most likely susceptible circuits within a spacecraft that can be affected by an ESD event. This preliminary work is needed to characterize the topology under which ESD events occur.

Figure 1 illustrates a simple example of an ESD event on a shielded cable from the solar array.

![Figure 1. ESD Events Due To Spacecraft Charging](image-url)
ANALYSIS OF RADIATED EMI FROM ESD EVENTS CAUSED BY SPACE CHARGING

II. EMI Coupling Phase: This is the second step in EMI analysis from ESD. Because of the complex physics involved in conducted and radiated EMI coupling, an approximation technique consists of the use of L,R,C (inductive, resistive, capacitive) network representations between surface charging and possible ESD paths. This technique provides acceptable results for low frequency, typically less than 50 mHz ($\lambda \gg \ell$, $\ell$ being the size of the device affected by ESD and $\lambda$ being the wavelength) EMI analysis. The coupling phase task involves the development of an electronic network representation to model the coupling parameters between a charging surface and a susceptible device or conductive path. The network representation will be useful in the evaluation of several characteristic parameters of ESD events such as current and voltages at desired locations. Figure 2 describes a very simple EMI coupling scenario between a solar array (the charging surface) and a spacecraft shielded cable component. The ESD topology phase previously described is a prerequisite for the development of the coupling phase.

![Figure 2. Lumped Element Parameters For ESD Event Between Solar Array And Cable](image)

III. ESD Event Phase: This phase involves the use of SPICE or another circuit analysis code to calculate currents and voltages at discharge locations. Figure 3 describes a simplified LRC network representation of the ESD event for the example in Figure 2.

IV. Induced Current Propagation Phase: This final step involves the usage of method-of-moments in computational electromagnetics to model the conducted path(s) and the lumped-network representations of circuits and devices connected to such paths. The method-of-moments will calculate the noise current distribution at any location on the conducted path(s) or circuits. From an assumed current waveform the conducted noise emissions in the frequency domain can then be obtained. The calculated noise current spectrum is also used for evaluating the radiated EMI noise by using simple radiator models. Figure 5 describes method of moments modeling for evaluating the induced noise current on the shielded cable of Figure 4.
ANALYSIS OF RADIATED EMI FROM ESD EVENTS CAUSED BY SPACE CHARGING

Figure 3. Circuit Representation of Solar Array To Spacecraft ESD

Figure 4. Physical Representation of Shielded Cable Inside Spacecraft Structure
ANALYSIS OF RADIATED EMI FROM ESD EVENTS CAUSED BY SPACE CHARGING

Figure 5. Method Of Moment Modeling of Shielded Cable

Technical Rationale:

The emphasis on ESD through the years has been on minimizing the occurrence of such events through: (a) a better understanding of the charging processes that occur on different types of dielectric materials used in a spacecraft, (b) a better understanding of the space plasma environment, and (c) following several EMI rules concerning shielding, grounding and bonding. However, there is a need to analyze the EMI effects of actual electrostatic discharge processes because it is usually not possible to eliminate all sources of ESD.

Impact of Non-practice:

EMI problems resulting from ESD events can degrade the performance of spacecraft in flight, when it is too late to implement design fixes.

Related Practices:

1. Design and Analysis of Electronic Circuits for Worst Case Environments and Part Variations, Practice No. PD-ED-1212

2. Antenna Radiation Modeling, (to be supplied at a later date)

References:
